



Core-to-Core Program



# Development of ultra-low mass and high-rate capable RPC based on Diamond-Like Carbon electrodes for MEG II experiment

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26th-30th September 2022

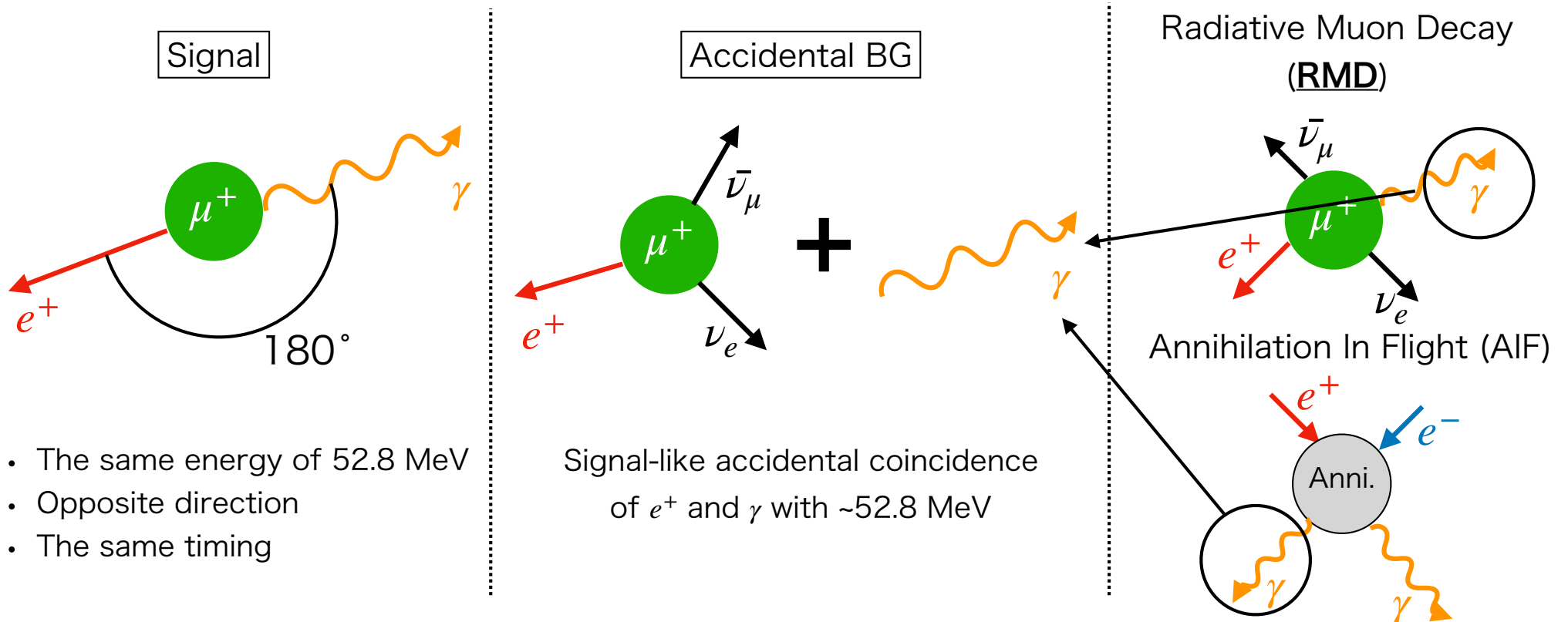
XVI Workshop of Resistive Plate Chambers and Related Detectors (RPC2022)

# Outline

- Introduction
  - MEG II experiment
  - DLC-RPC
- High-rate performance test with our 1st prototype detector
- Improved prototype test
- Summary and prospects

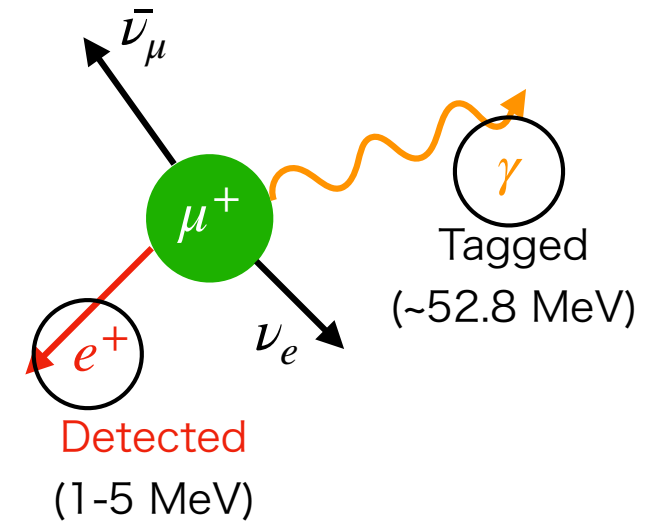
# MEG II signal and background

- MEG II searches for  $\mu \rightarrow e\gamma$  decay, one of charged lepton flavour violation (cLFV) channels
- Dominant background is accidental coincidence of BG- $e^+$  and BG- $\gamma$

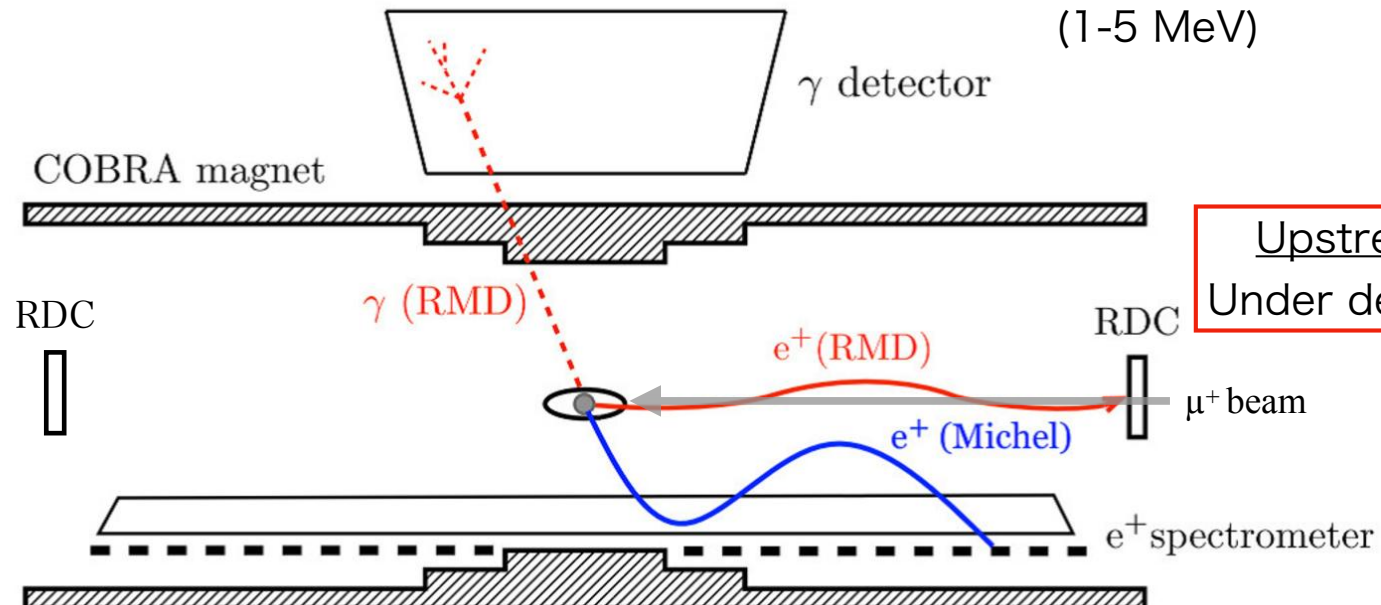


# Radiative Decay Counter

- Radiative Decay Counter (RDC) detects RMD  $e^+$  with 1-5 MeV to tag BG- $\gamma$ 
  - RMD  $\gamma$  tagged by time difference between  $\gamma$  in  $\gamma$  detector and  $e^+$  in RDC
  - Low-energy  $e^+$  fly around beam axis due to magnet

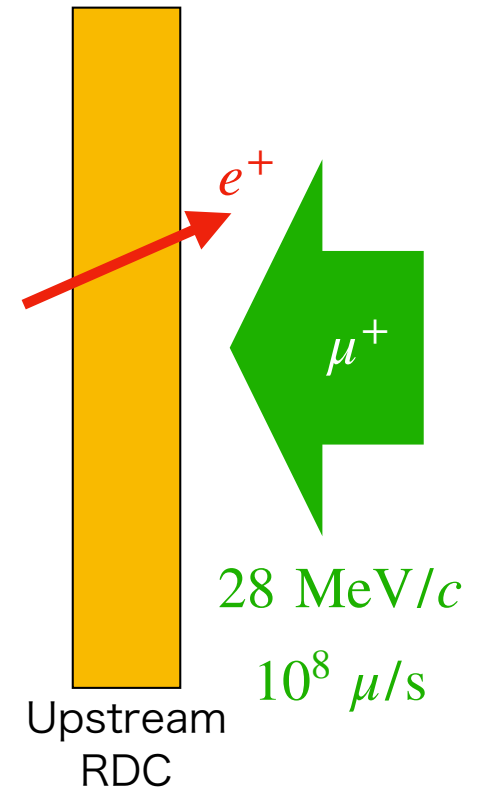


Downstream RDC  
Already developed  
and installed



# Requirements for upstream RDC

1.  $<0.1\%$   $X_0$  material budget
  - $\mu$  beam with  $28 \text{ MeV}/c$  must pass through the detector
2. Rate capability for  $10^8 \mu/s$  ( $4 \text{ MHz}/\text{cm}^2$  at centre)
3. Radiation hardness for  $>60$  weeks operation
4. 90% efficiency for RMD  $e^+$  with 1-5 MeV
5. 1 ns timing resolution
6. 20 cm diameter detector size

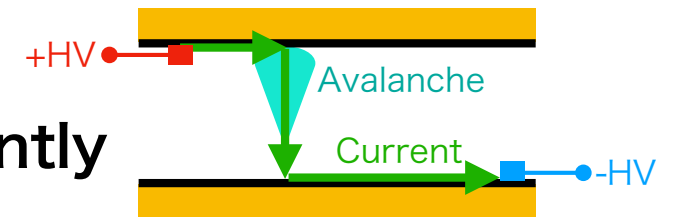
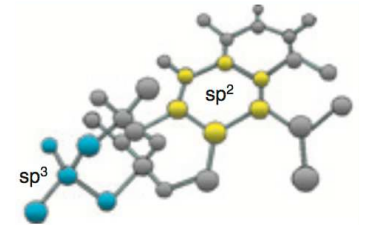


**Ultra-low mass and high-rate capable RPC  
with Diamond-Like Carbon electrodes  
for upstream RDC**

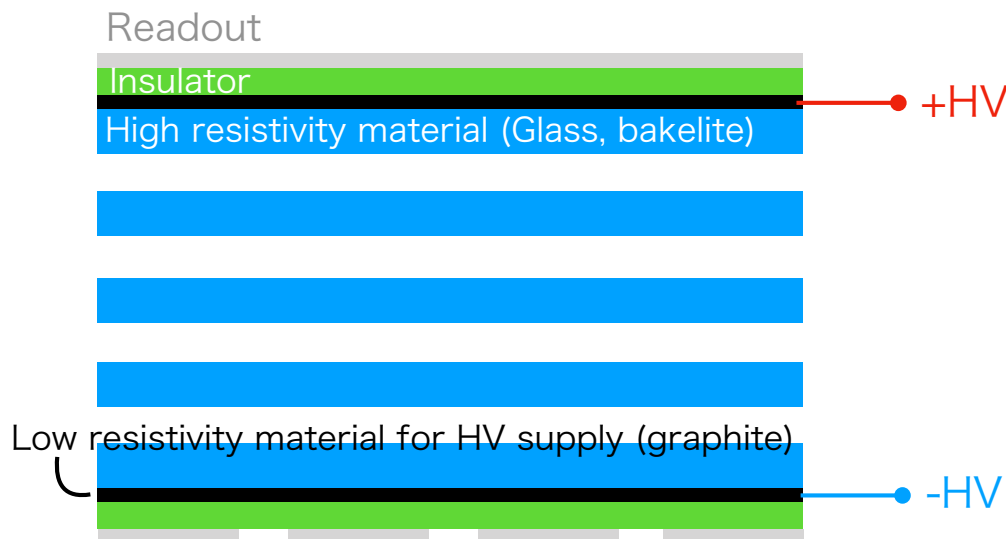
# DLC-RPC (1)

- RPC with DLC electrodes (DLC-RPC)
  - DLC sputtered on 50  $\mu\text{m}$ -thick polyimide (PI) foil
  - **Material budget can be suppressed**
  - Electrodes decoupled by PI
  - **HV applied for each layer independently**

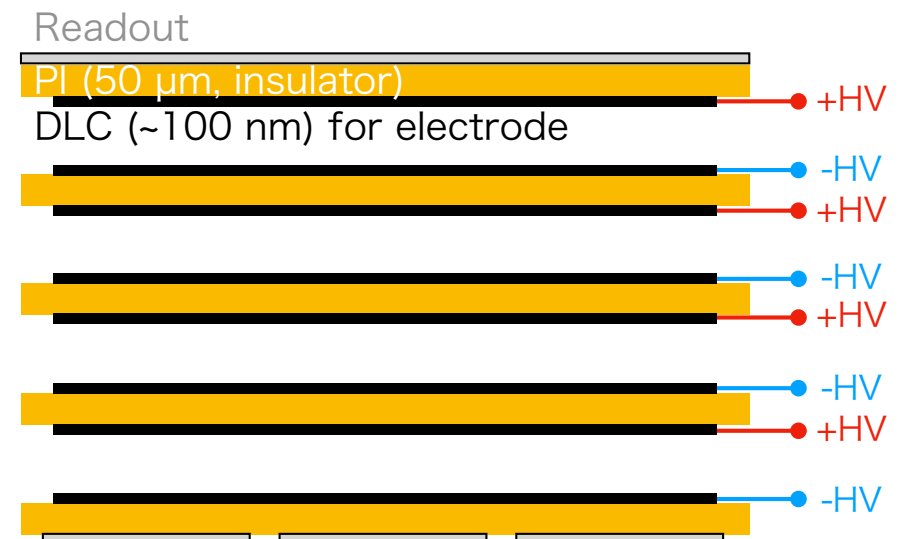
Diamond-Like Carbon



(Conventional) bulk RPC

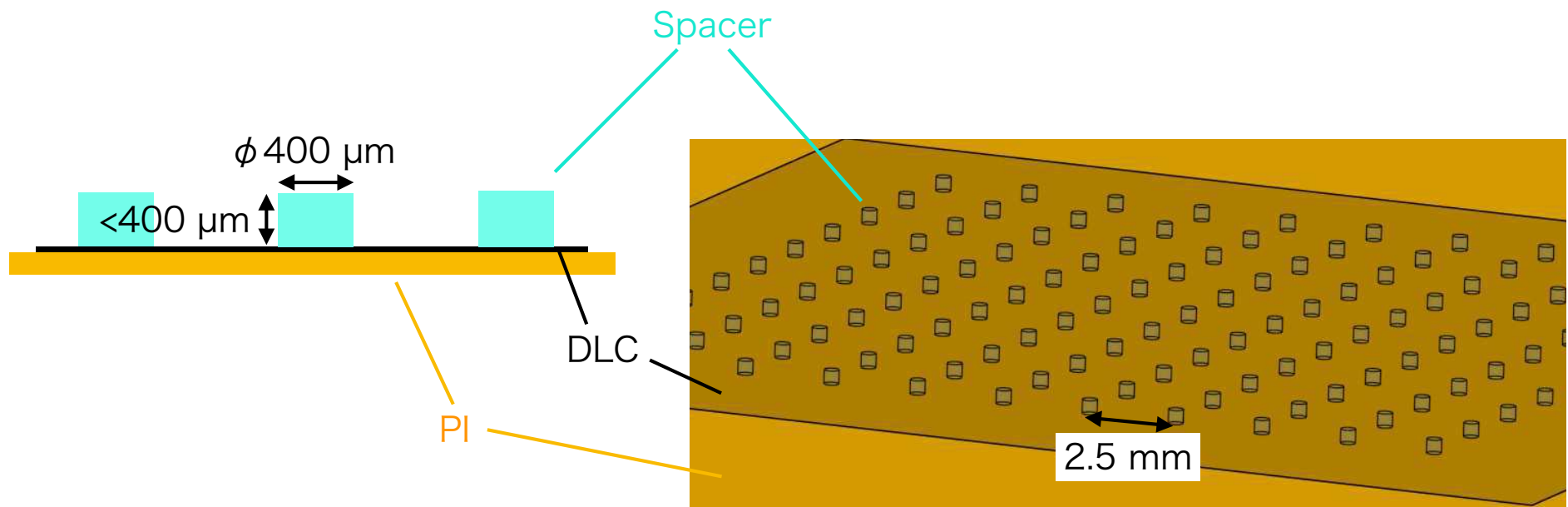


DLC-RPC



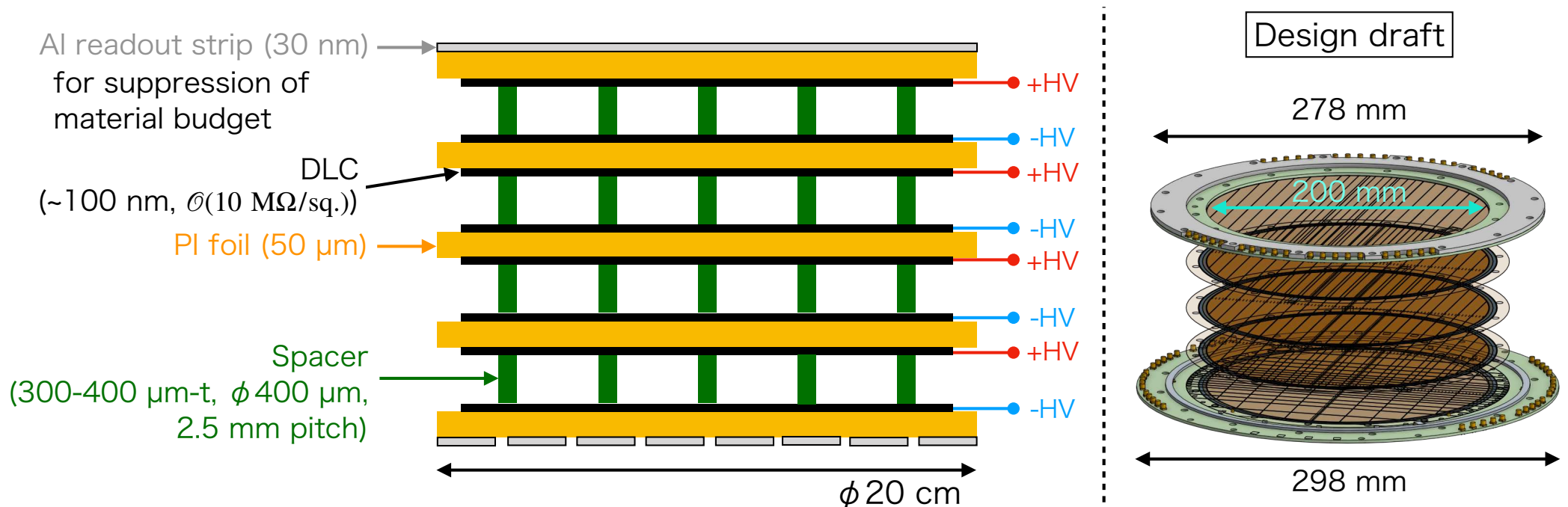
# DLC-RPC (2)

- Spacers formed using photolithographic technology
  - Should be careful to make flat thin film



# DLC-RPC for MEG II

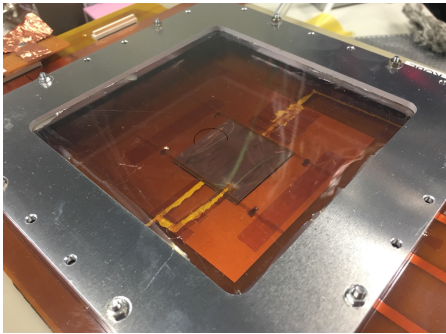
- Electrodes stacked for higher detection efficiency
  - Efficiency with  $n$  layers:  $\epsilon_n = 1 - (1 - \epsilon_1)^n$
- **4 layers** limited by material budget
  - 50  $\mu\text{m}$ -thick PI foil: 0.018%  $X_0$
- $\epsilon_1 > 40\%$  for 90% detection efficiency with 4 layers
  - **>300  $\mu\text{m}$  gap thickness** needed



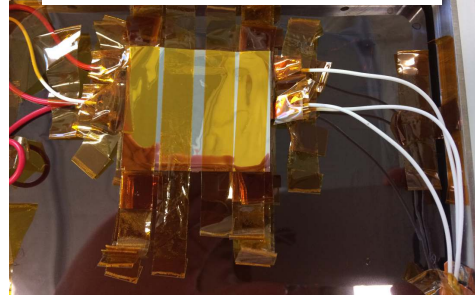


# R&D history

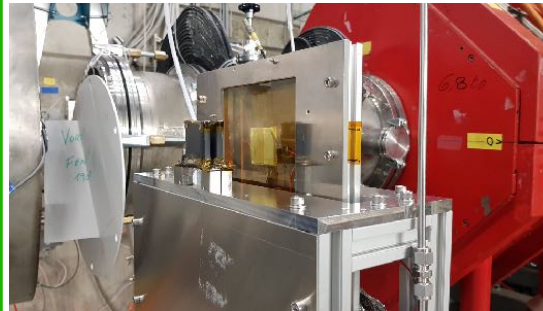
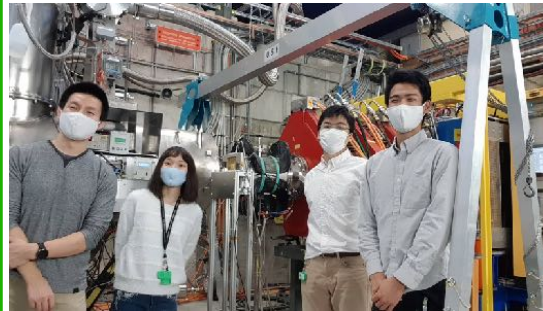
R&D for fast timing RPC



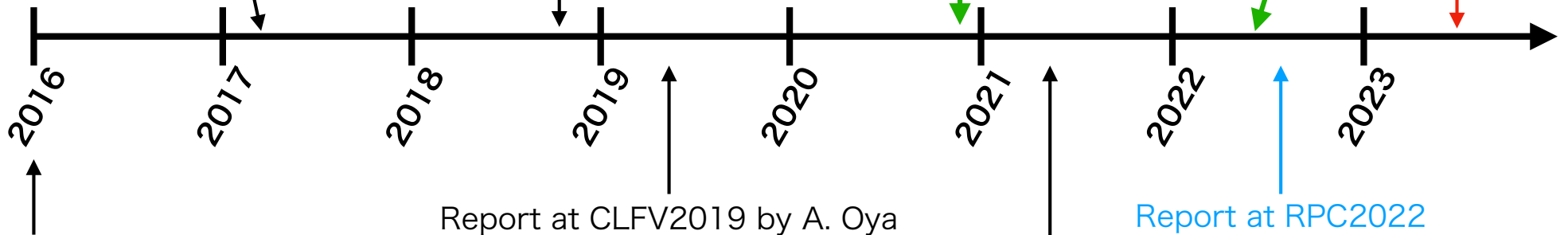
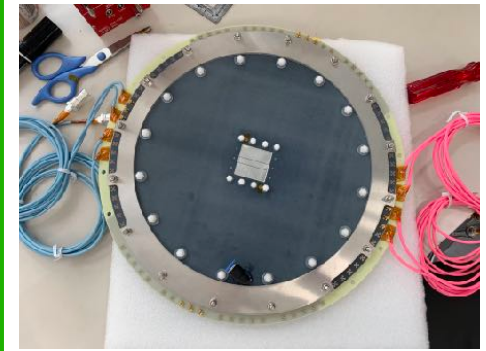
Prototype for MEG II



$\mu^+$  beam test at PSI



Improved detector R&D



**New RPC idea by A. Ochi**

Adopted for Grant-in-Aid for Challenging Research

Report at CLFV2019 by A. Oya

Report at TIPP2021 by A. Oya

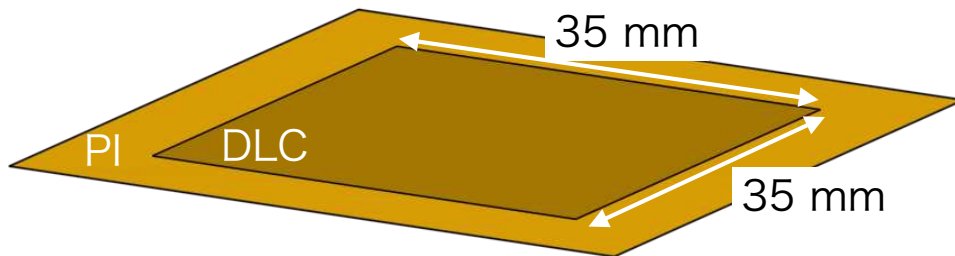
Report at RPC2022

Aim to install in MEG II

# Outline

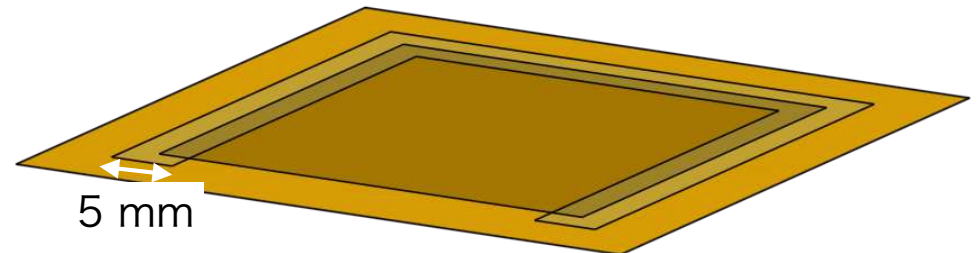
- Introduction
- High-rate performance test with our 1st prototype detector
  - 1st prototype detector
  - High rate performance
- Improved prototype test
- Summary and prospects

# Electrode production



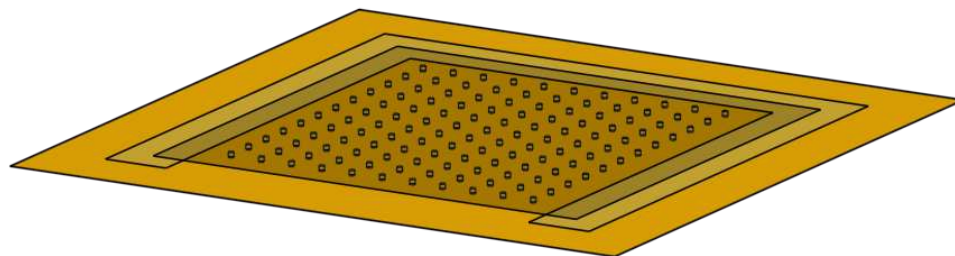
## 1 - DLC sputtered on Kapton foil

- DLC thickness ~ 800 Å



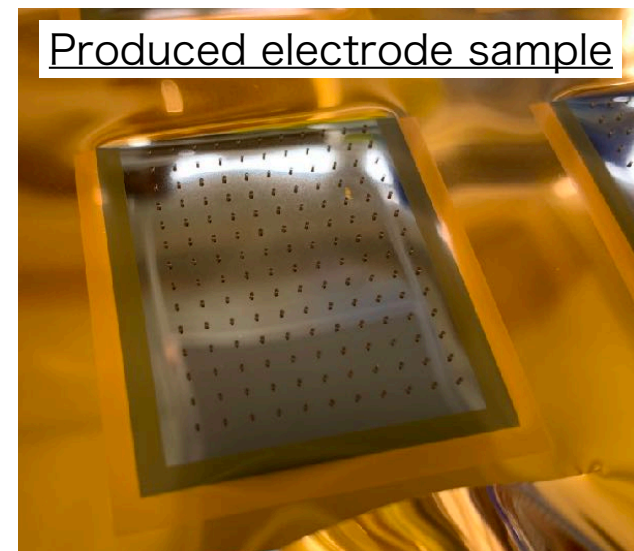
## 2 - Insulation cover deposited

- 25 μm-thick photo-resist
- Deposited on DLC boundary

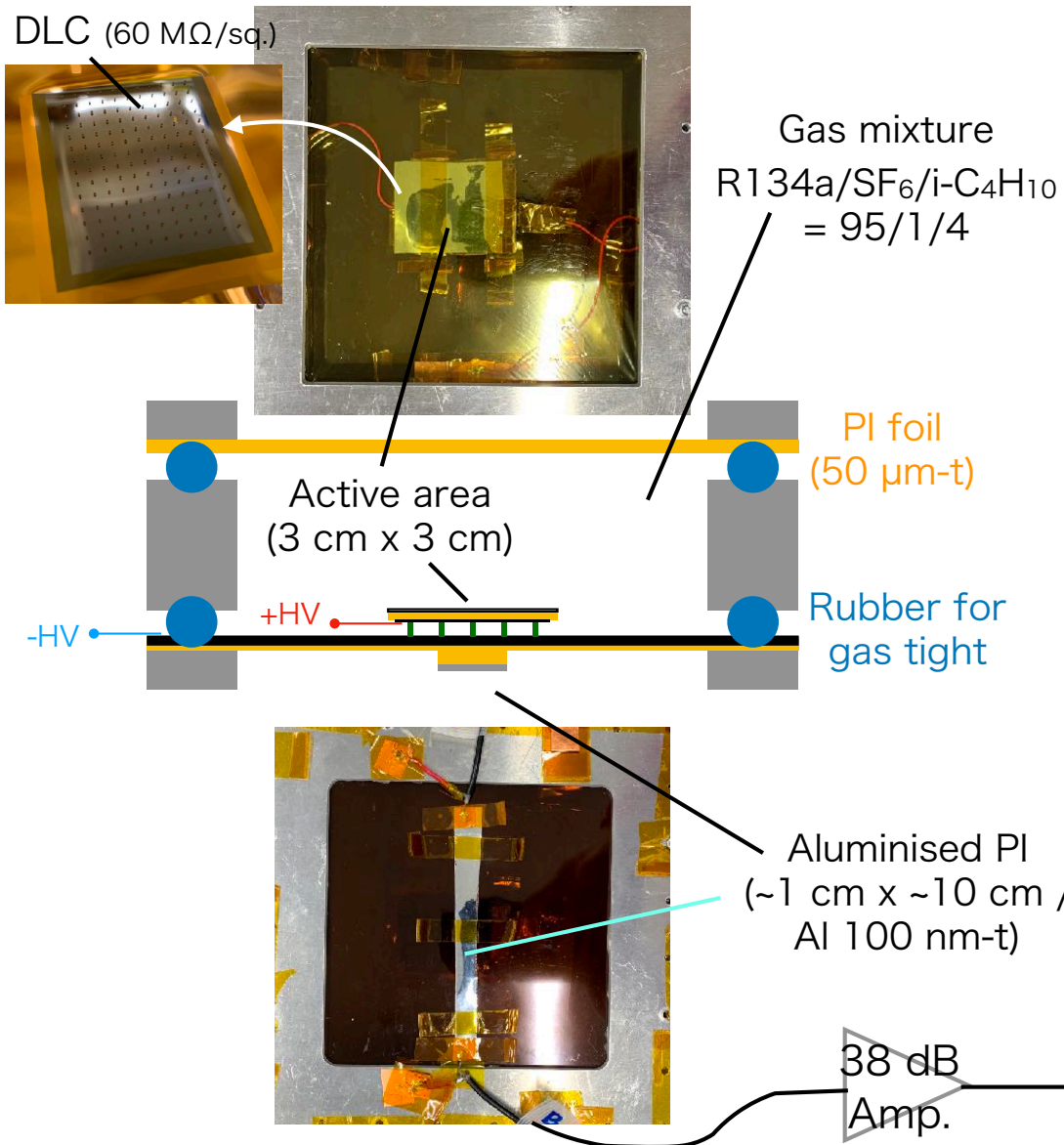


## 3 - Spacers formed

- 384 μm-thick photo-resist
- 2.5 mm pitch



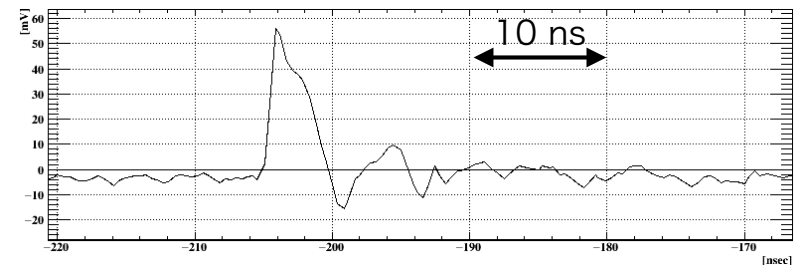
# 1st prototype detector



## Performance overview

at  $\mathcal{O}(1 - 10 \text{ kHz/cm}^2)$

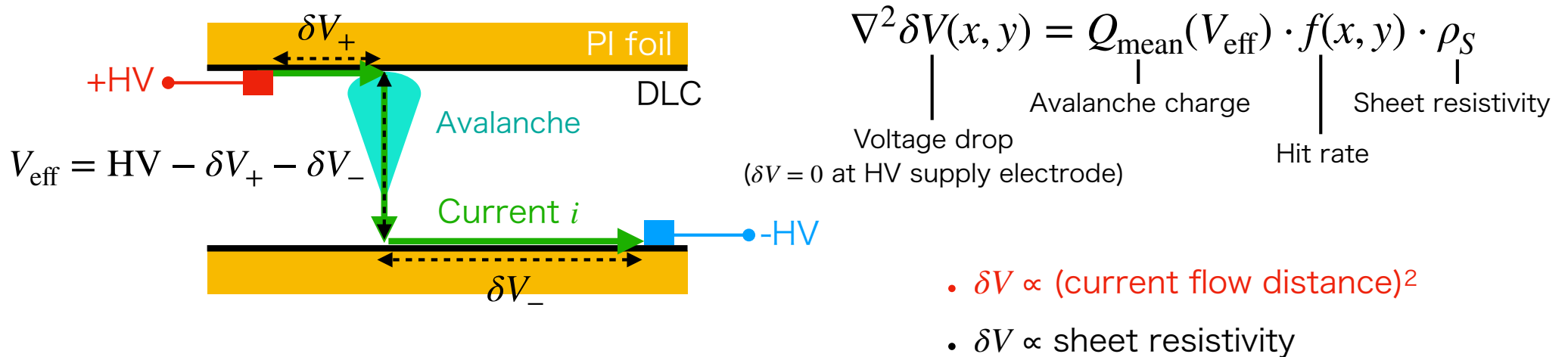
- Successful operation with ultra-low mass design ( $<0.1\% X_0$ )
  - Detect low-momentum  $\mu^+$  (28 MeV/c) penetrating detector
- 60% MIP efficiency achieved with single layer
- 85% MIP efficiency achieved with multi layers
  - Due to non-flatness of electrode
- 170 ps timing resolution



Waveform digitiser  
(DRS4, 700 MHz)

# High rate capability of DLC-RPC

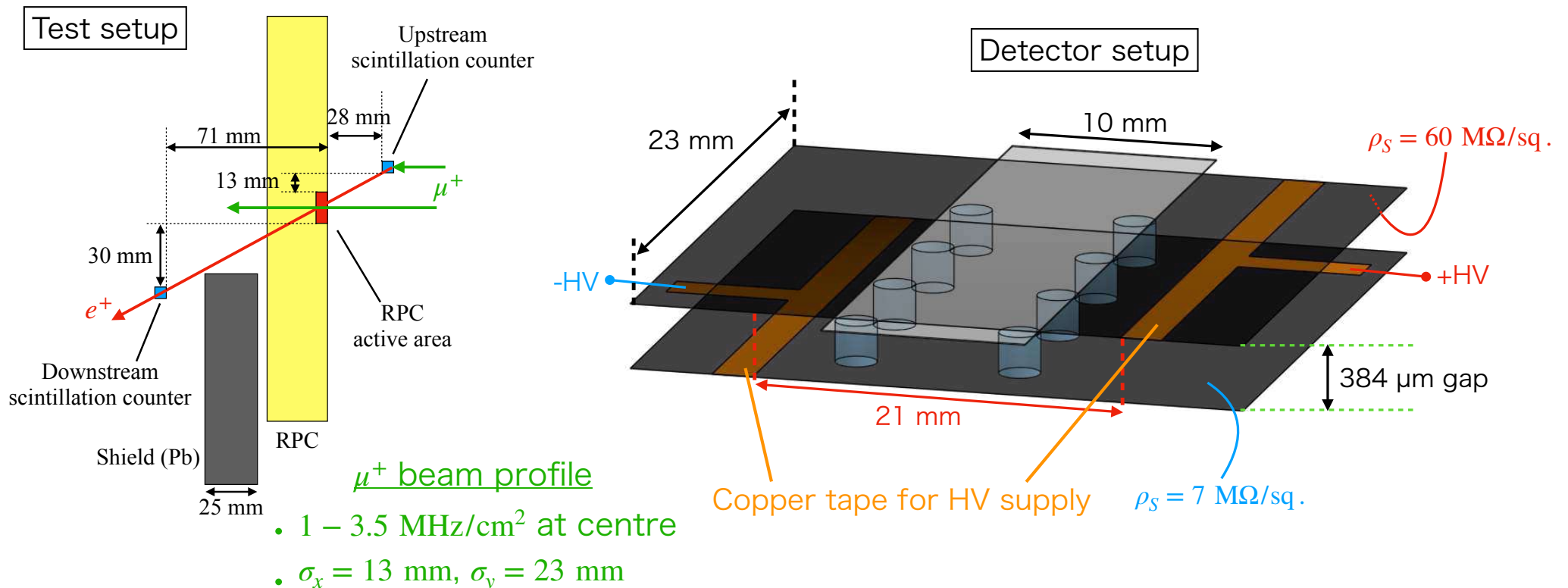
- Large current on resistive electrodes at high rate
- Voltage drop  $\delta V$  reduces effective applied HV  $V_{\text{eff}}$
- Gas gain reduction





# High-rate performance test

- MIP  $e^+$  detection in low-momentum  $\mu^+$  beam
  - $\mu^+$  beam at piE5 at Paul Scherrer Institut



# Performance at high rate

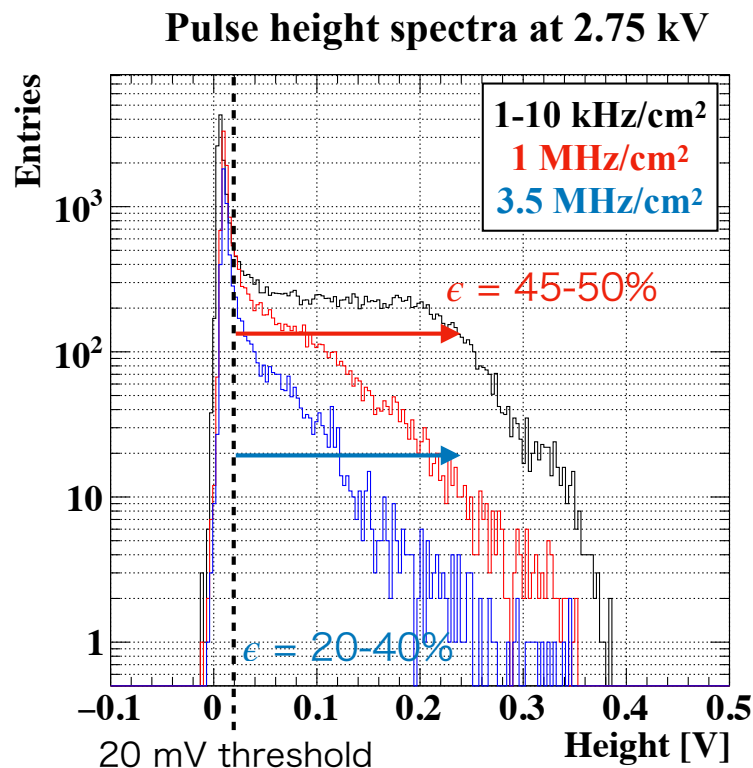
Detection efficiency:

- 45-50% at 1 MHz/cm<sup>2</sup>
- 20-40% at 3.5 MHz/cm<sup>2</sup>

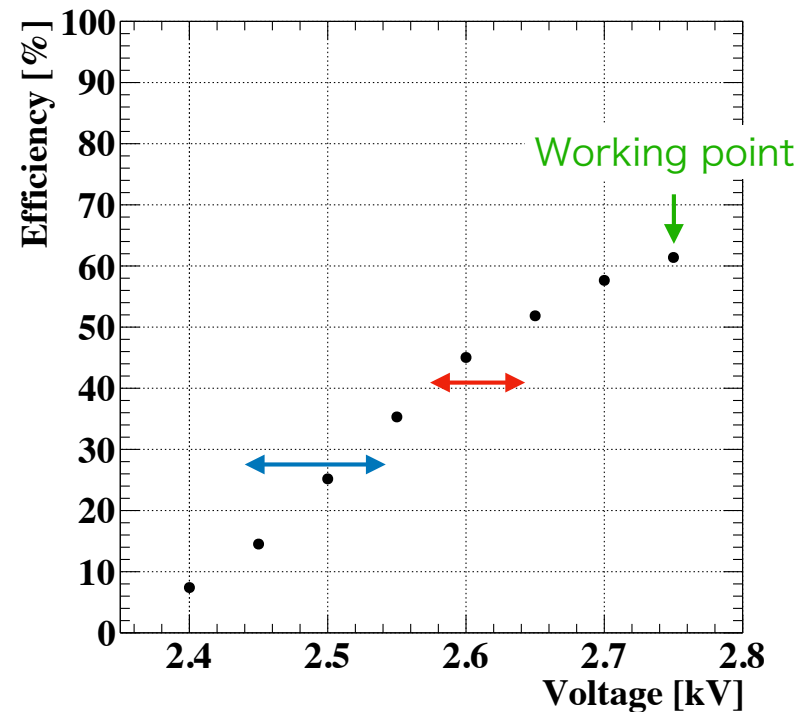
Calculated voltage drop:

- 110-170 V at 1 MHz/cm<sup>2</sup>
- 210-310 V at 3.5 MHz/cm<sup>2</sup>

→ 1 MHz/cm<sup>2</sup> rate capability

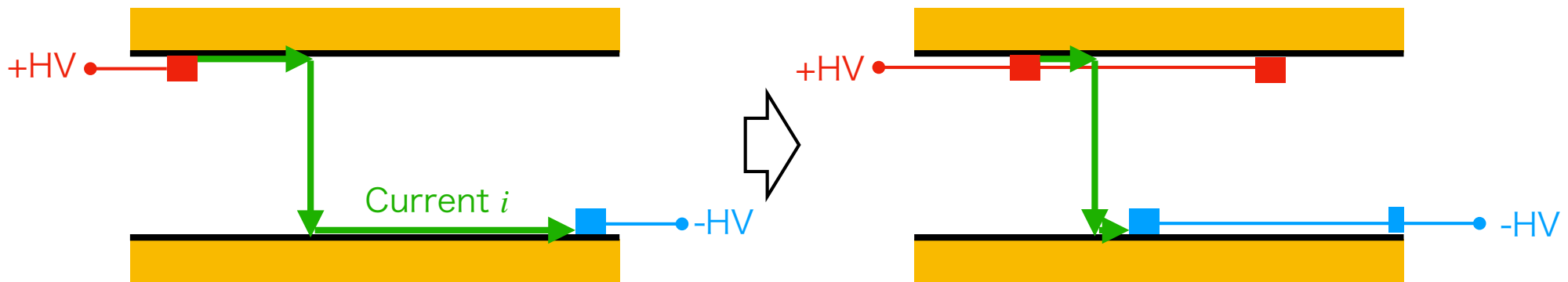


Detection efficiency at  $\mathcal{O}(1 - 10 \text{ kHz/cm}^2)$



# Electrode to be improved

- Voltage drop should be suppressed for higher rate capability
    - **HV supply segmented for short current flow** (1 cm pitch)
      - Voltage drop  $\propto$  (current flow distance)<sup>2</sup>
      - Need also for scalability
    - **Resistivity should be low** (10 M $\Omega$ /sq.)
      - Voltage drop  $\propto$  (sheet resistivity)
      - Not too low for stable operation
- Voltage drop will be 60-80 V at 4 MHz/cm<sup>2</sup>**

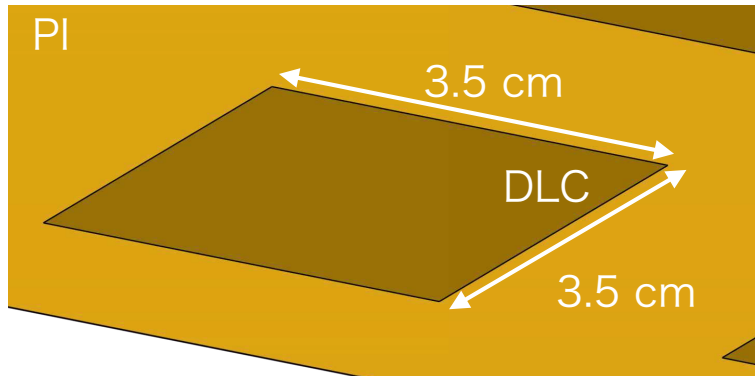




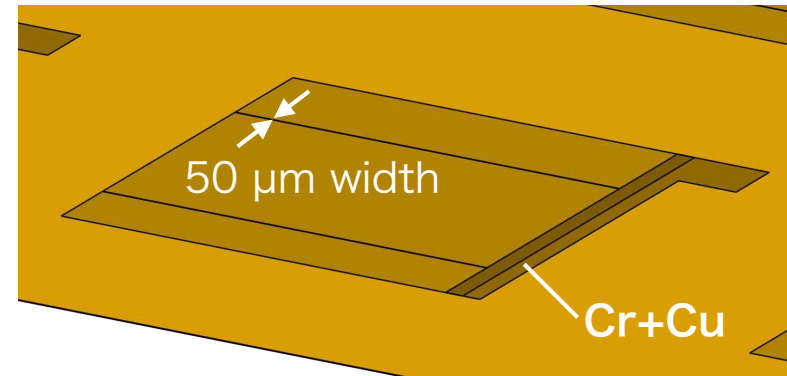
# Outline

- Introduction
- High-rate performance test with our 1st prototype detector
- Improved prototype test
  - Develop improved electrode
  - Performance test at  $\mathcal{O}(10 \text{ kHz/cm}^2)$
- Summary and prospects

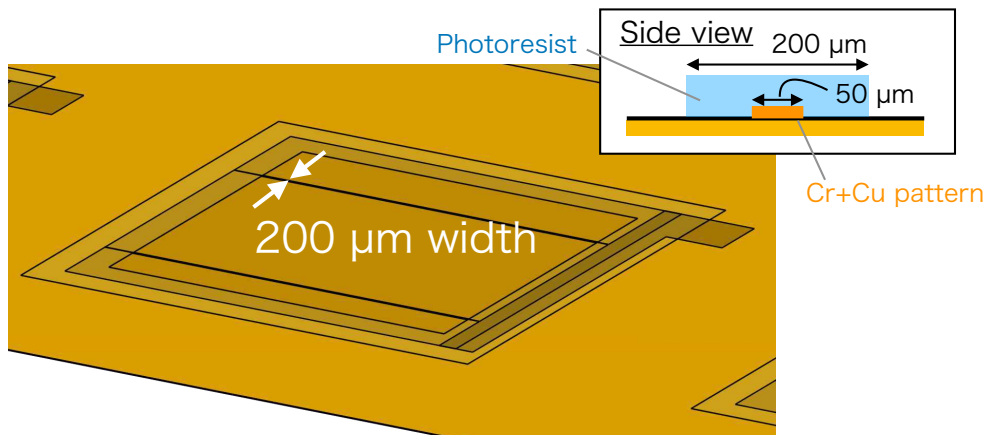
# Electrode production



1 - DLC sputtered on Kapton foil

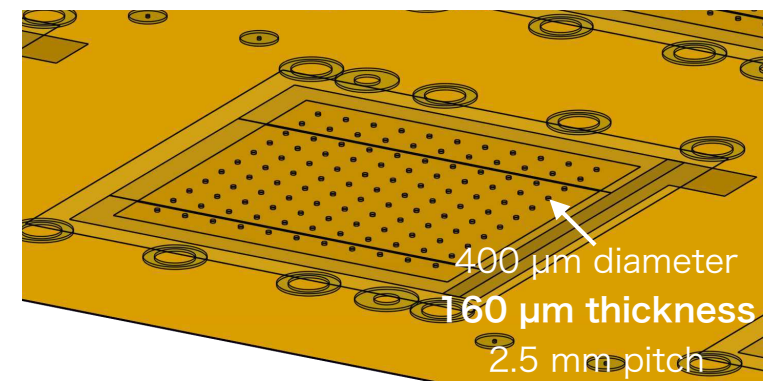


2 - Conductive pattern implemented  
• Cr can be well-connected onto DLC



3 - Insulation cover deposited

- 25 μm-thick photo-resist
- Deposited on conductive pattern and DLC boundary



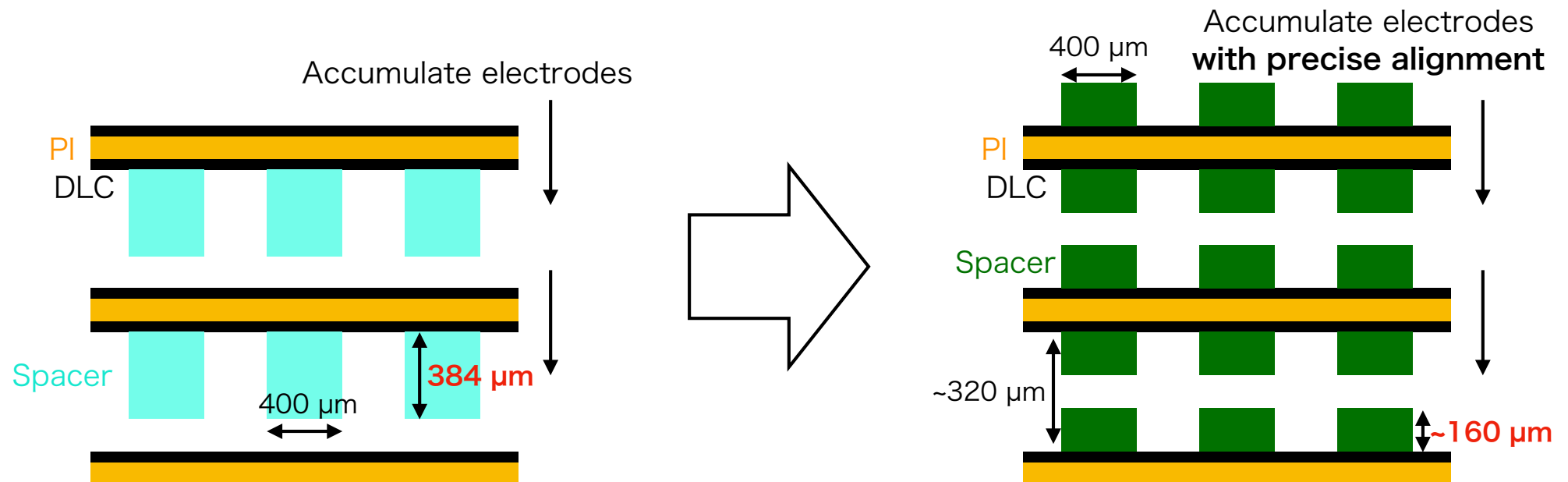
4 - Spacers formed

- ~160 μm-thick photo-resist
- Doubly accumulated for >300 μm gap thickness

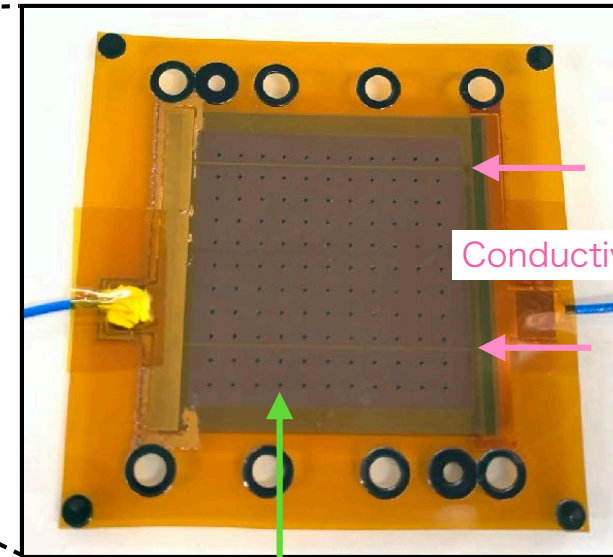
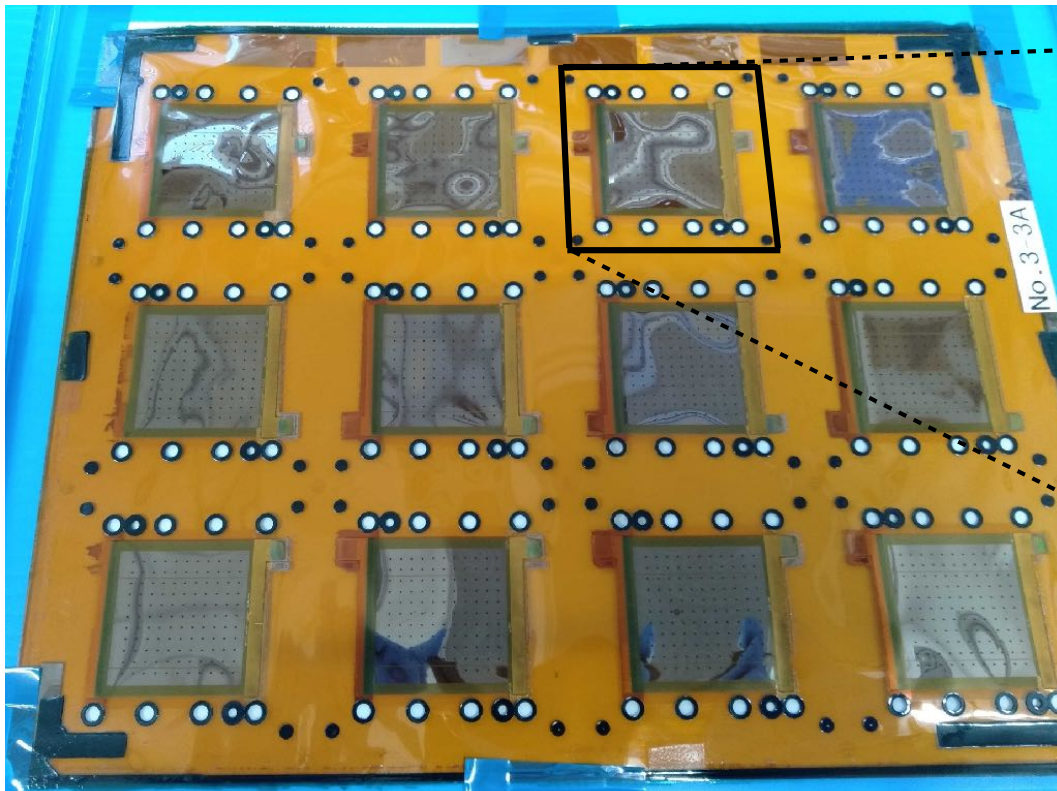
# Spacer formation

- Previous spacer material production cancellation
- ➔ New spacer material used
  - $>300\ \mu\text{m}$ -thick spacers cannot be formed
- Strategies for enough gap thickness
  - **Form  $\sim 200\ \mu\text{m}$ -thick spacers**
  - **Doubly accumulate spacers with precise alignment**

300  $\mu\text{m}$  gap thickness needed for enough efficiency



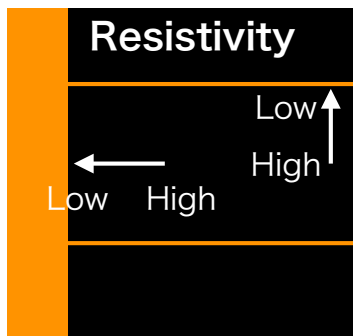
# Produced electrode



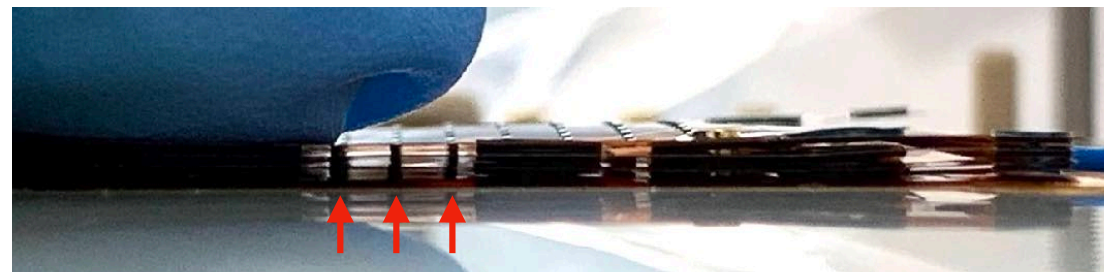
Conductive pattern

~160  $\mu\text{m}$ -thick spacers  
(2.5 mm pitch)

Good connection between Cr & DLC

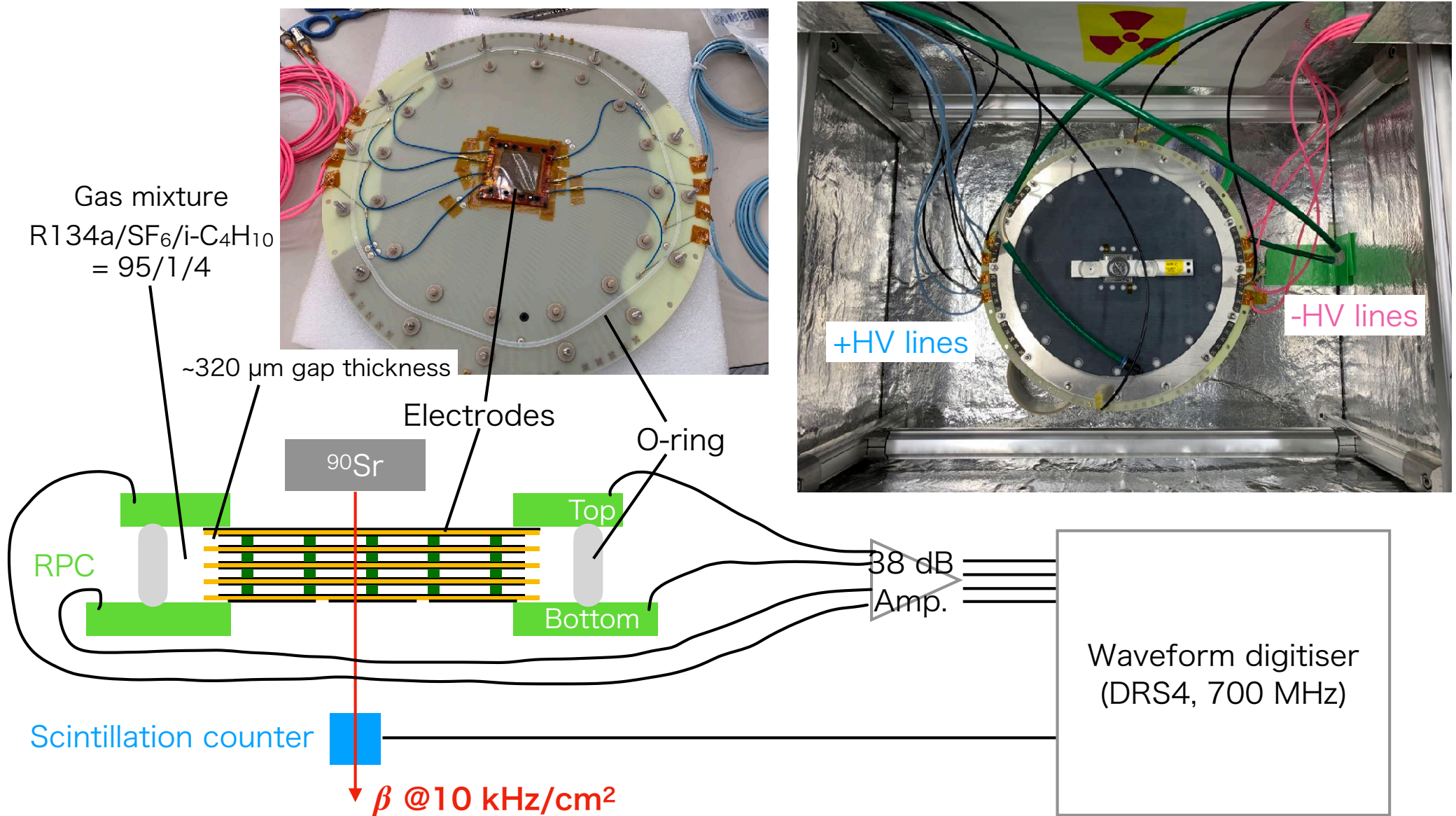


Good alignment





# Test setup

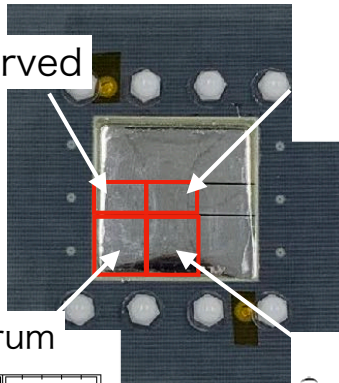


# Gap thickness non-uniformity

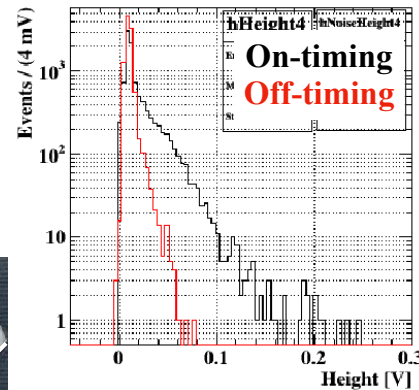
## Non-uniformity in a layer

Detector response depends on  $\beta$  incident position

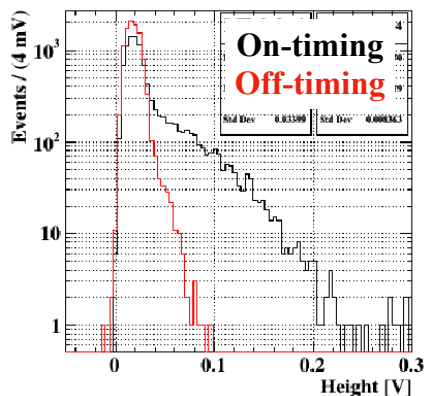
No signal observed



Height spectrum

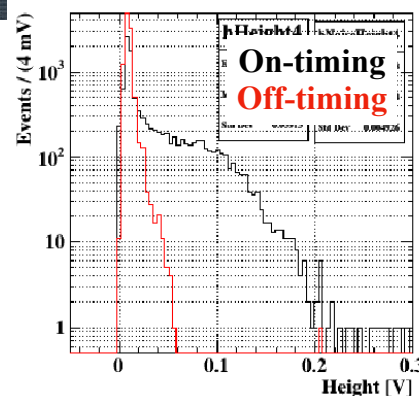


Height spectrum



Large noise in this strip

Height spectrum



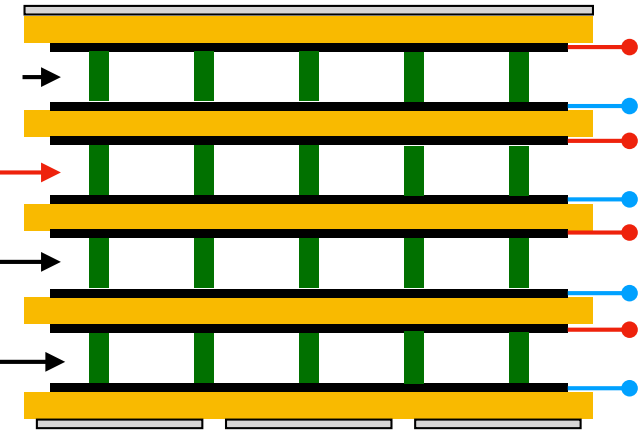
## Non-uniformity among different layers

A few % signal @2600 V

46% eff. @2500 V

14% eff. @2250 V

5% eff. @2200 V



Different gap thickness among different layers

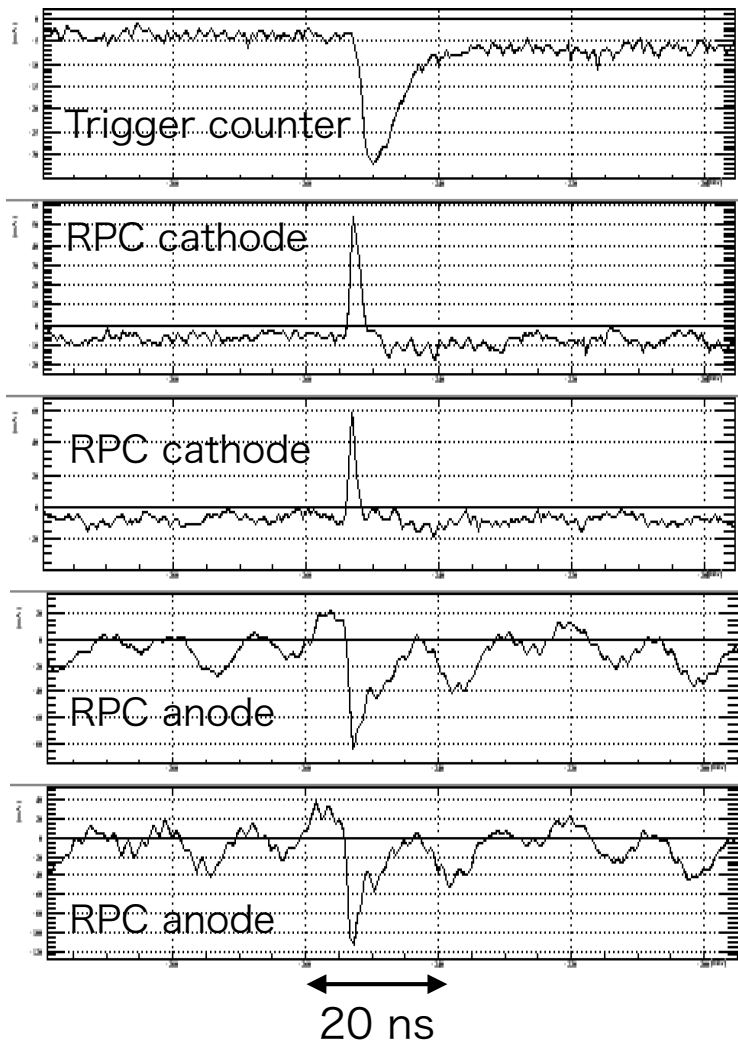
## Expected cause



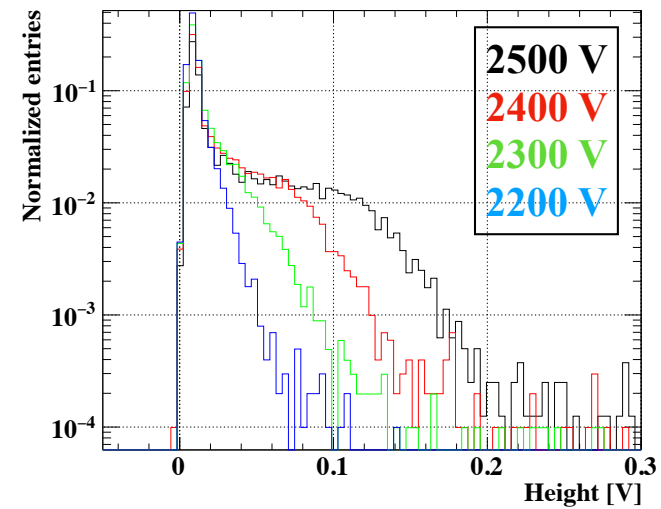
To be controlled by negative pressure in gas gap

# Performance with single layer

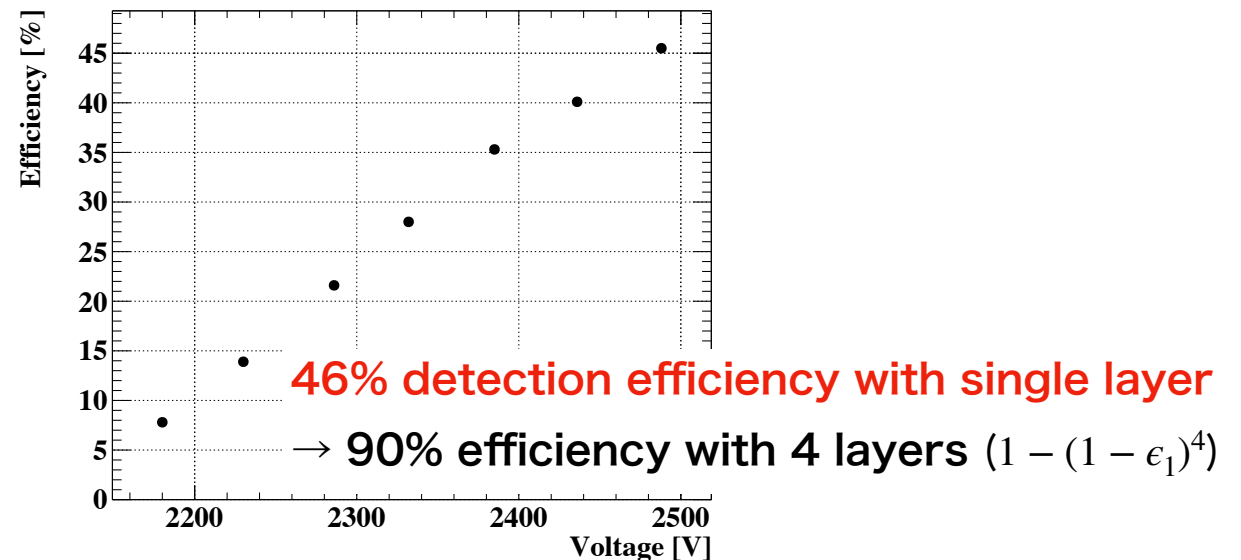
Event display



Pulse height spectra



Efficiency vs voltage



# Outline

- Introduction
- High-rate performance test with our 1st prototype detector
- Improved prototype test
- Summary and prospects



# Summary & prospects

- Ultra-low mass and high-rate capable RPC with DLC electrodes is under development
  - For BG tagging in MEG II experiment
- **1 MHz/cm<sup>2</sup> rate capability** achieved with 1st prototype detector
  - 45-50% detection efficiency with single layer even with 110-170 V drop
- Improved prototype detector produced and tested
  - Cr+Cu pattern formed on DLC for HV supply segmentation
  - **46% detection efficiency achieved with single layer** at 10 kHz/cm<sup>2</sup>
  - Gap thickness to be controlled
- Performance to be evaluated with  $\mu^+$  beam at 4 MHz/cm<sup>2</sup>

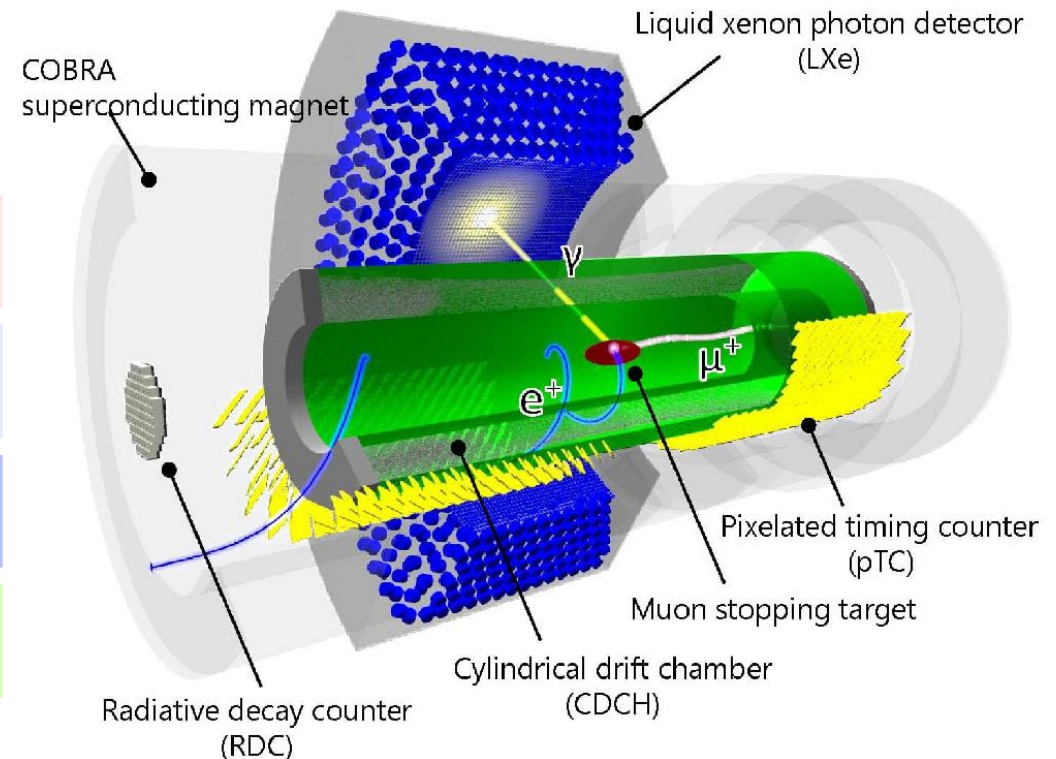
# Backup

# MEG II experiment

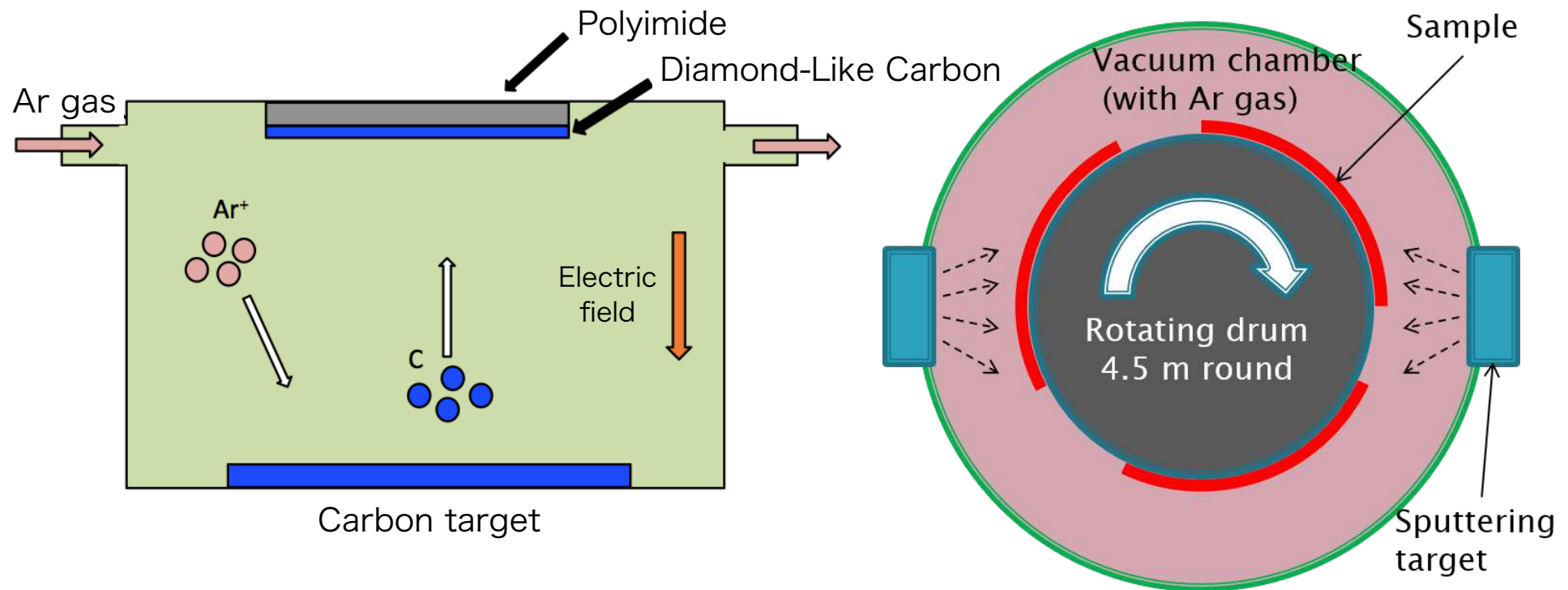
- MEG II searches for  $\mu^+ \rightarrow e^+\gamma$  decay with the sensitivity of  $6 \times 10^{-14}$ 
  - SM +  $\nu$  osc:  $\mathcal{B}(\mu^+ \rightarrow e^+\gamma) \sim 10^{-54}$
  - BSM (SUSY-GUT, SUSY-seesaw):  $\mathcal{B}(\mu^+ \rightarrow e^+\gamma) \sim 10^{-11} - 10^{-15}$

		世代 Generation			
		I	II	III	
電荷 Charge	スピン Spin				
クォーク Quarks	+2/3	1/2	u up	c charm	t top
	-1/3	1/2	d down	s strange	b bottom
レプトン Leptons	-1	1/2	e electron	$\mu$ muon	$\tau$ tau
	0	1/2	$\nu_e$ electron neutrino	$\nu_\mu$ muon neutrino	$\nu_\tau$ tau neutrino

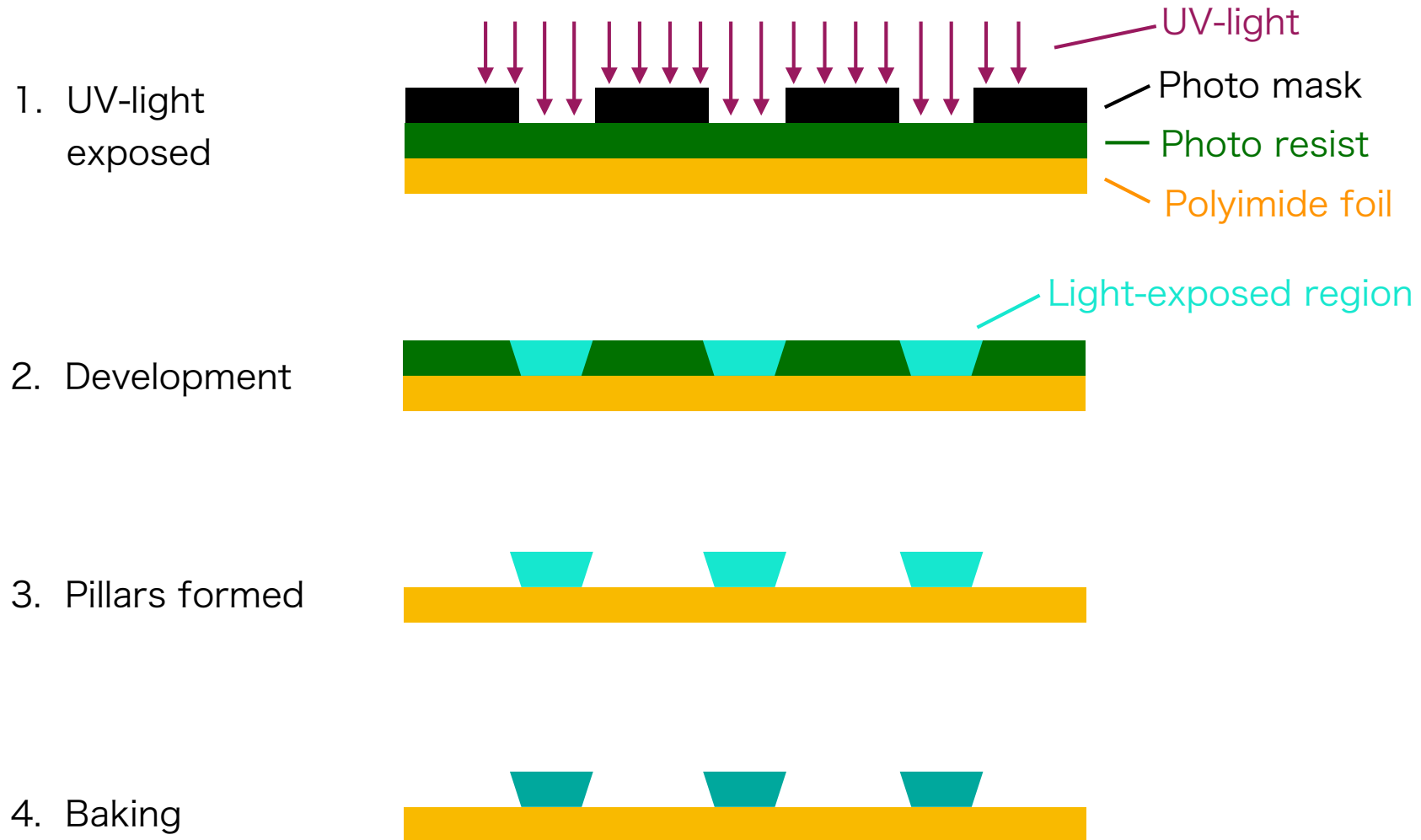
↔ CKM ↔  
↔  $\nu$  oscillation ↔



# DLC sputtering



# Spacer formation

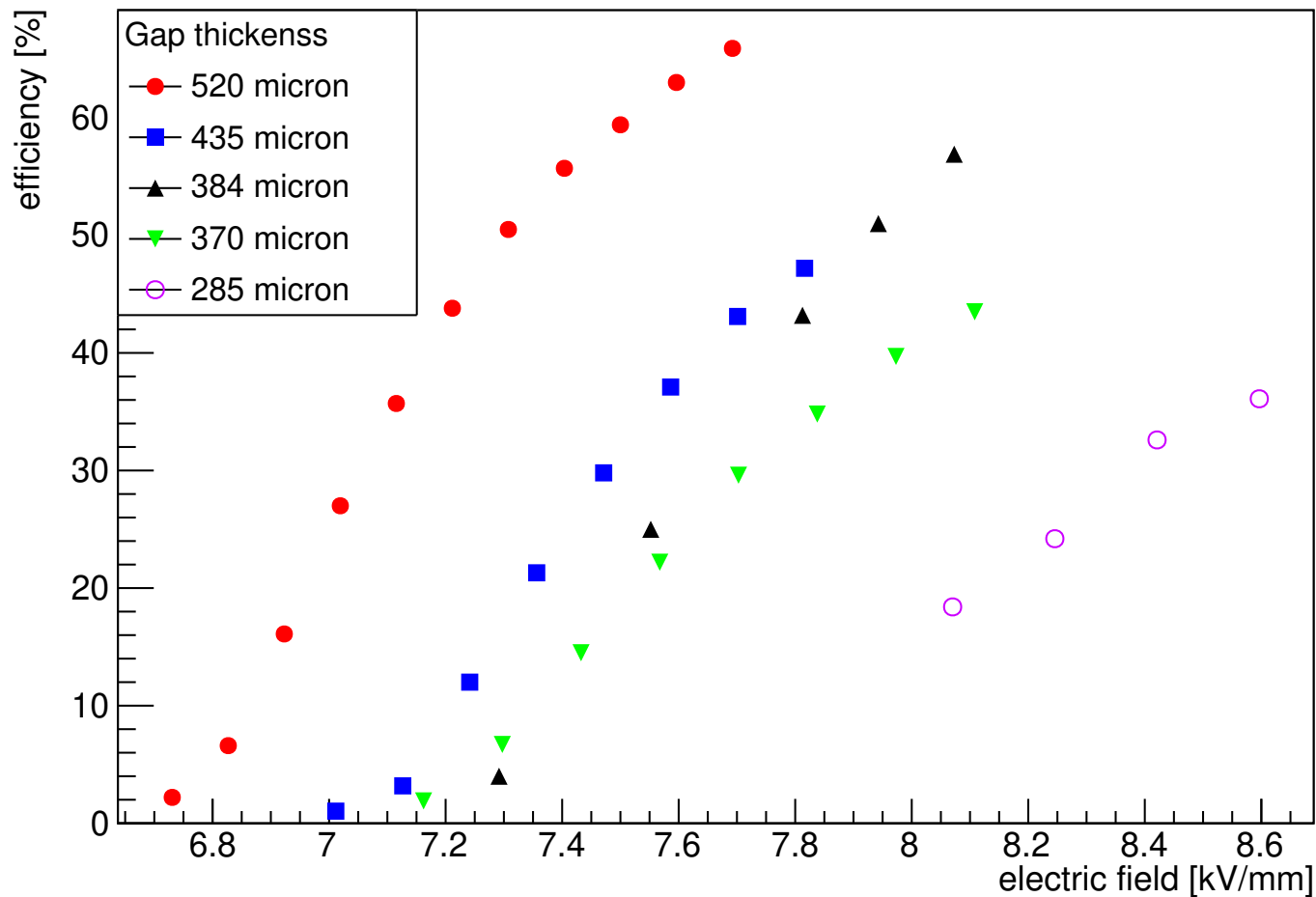


# Efficiency vs gap thickness

efficiency vs electric field

Spacers:

- 384  $\mu\text{m}$  - photolithographic pillars
- The others - fishing line

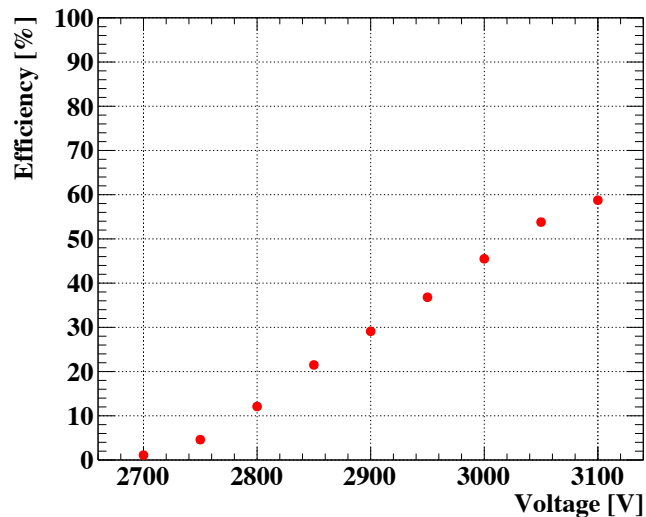


# Efficiency improved by stacking

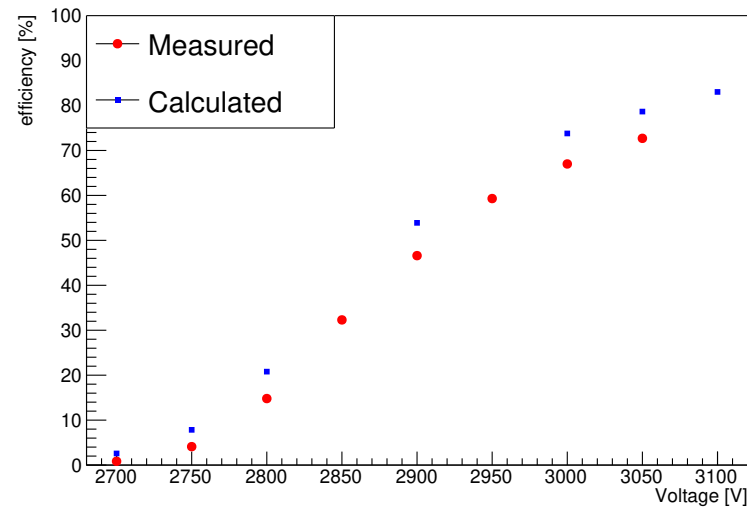
Gas mixture  
R134a/SF<sub>6</sub> = 93/7

Calculated by  
 $\epsilon_n = 1 - (1 - \epsilon_1)^n$

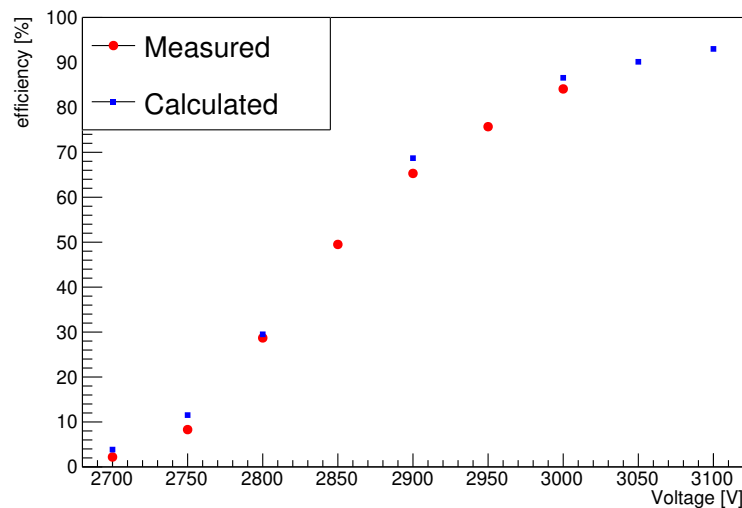
384 micron - 1 layer



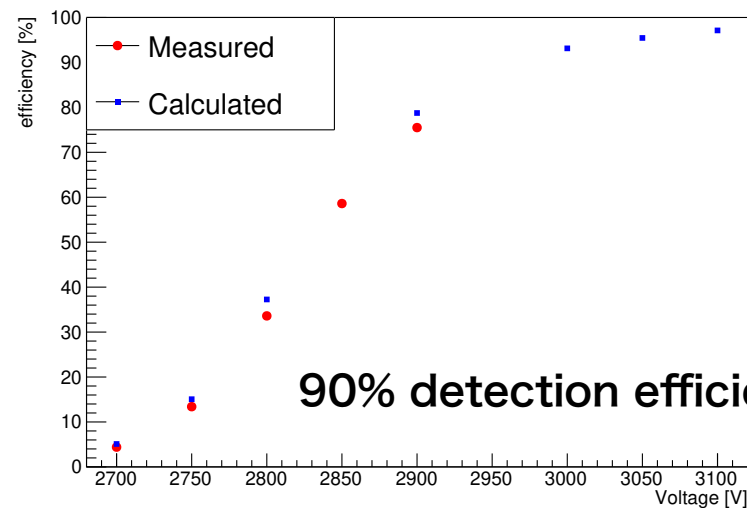
384 micron - 2 layer



384 micron - 3 layer



384 micron - 4 layer

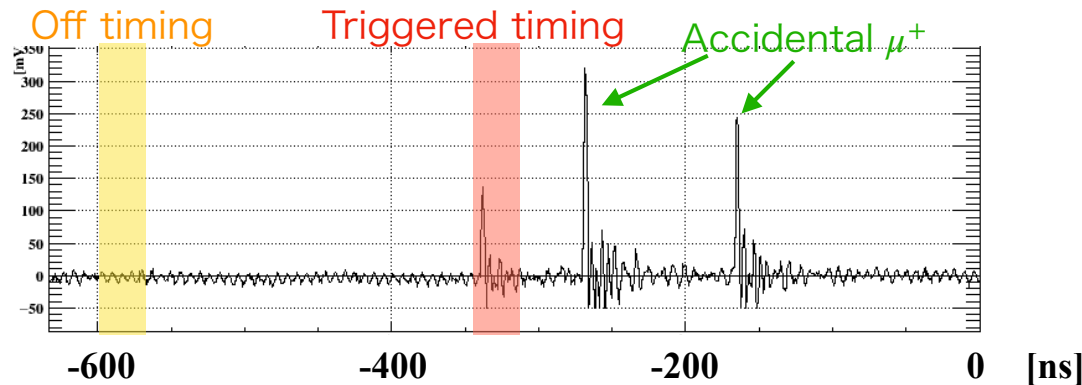


90% detection efficiency is achievable

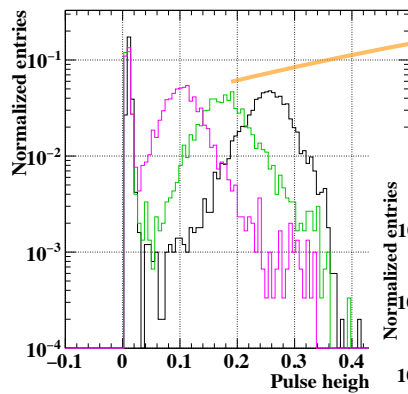
# Voltage drop evaluation

Observe voltage drop of 110-170 V

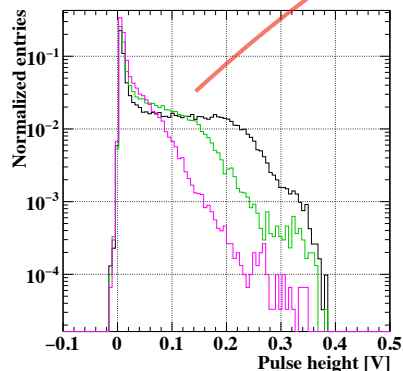
Details are shown in  
A. Oya, et al, "Development of high rate capable and ultra-low mass Resistive Plate Chamber with Diamond-Like Carbon", TIPP2021



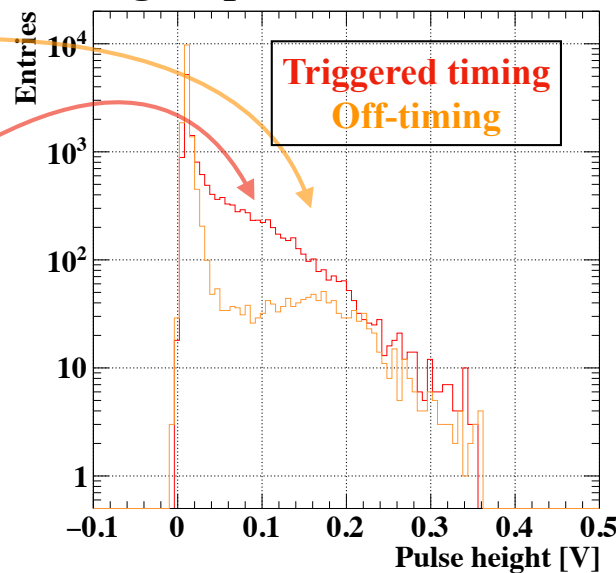
$\mu$  height spectra



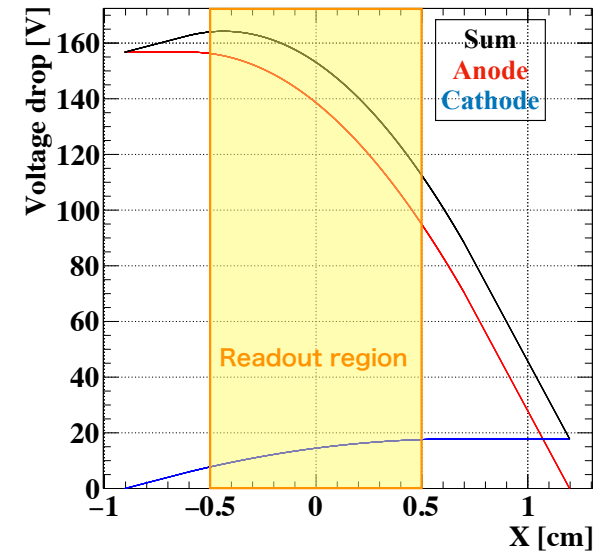
e height spectra



Height spectra at 1 MHz/cm<sup>2</sup>



Calculated voltage drop

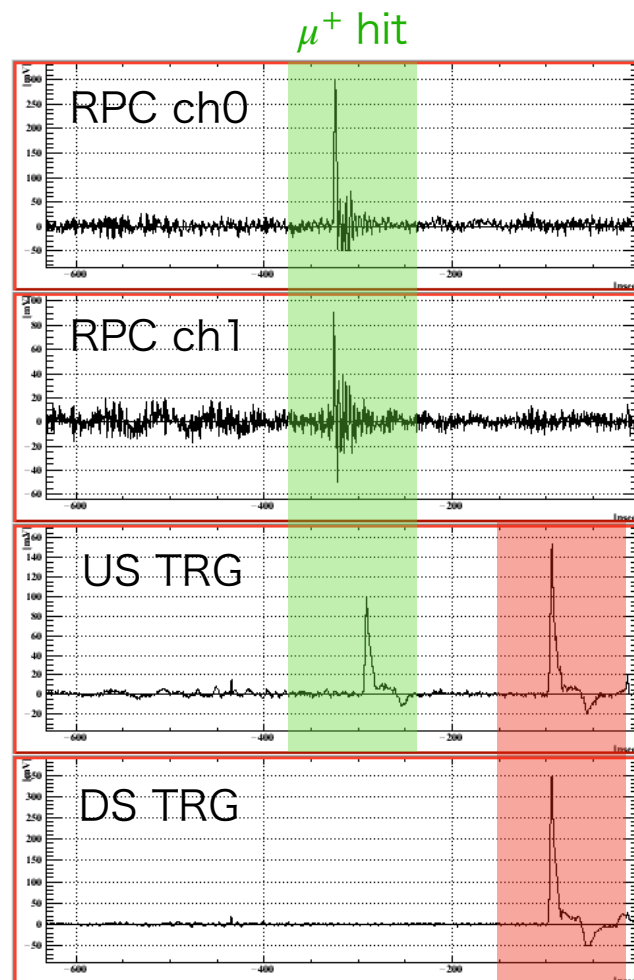
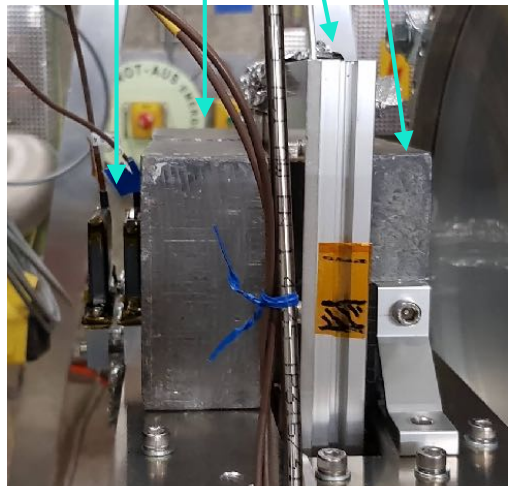
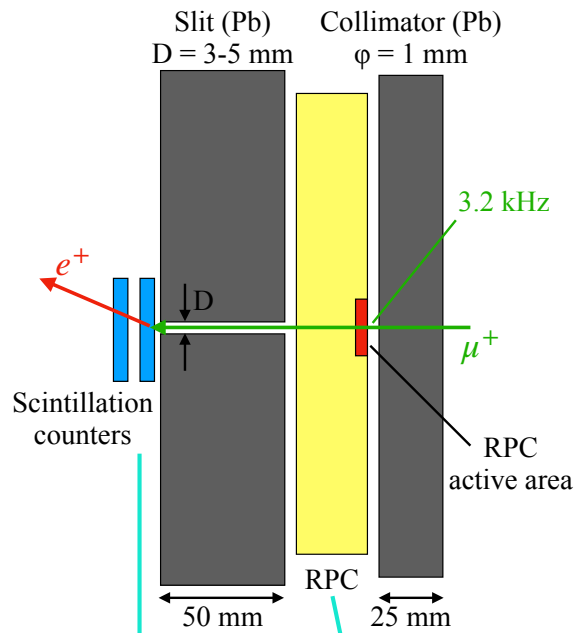


$$\nabla^2 \delta V(x, y) = Q_{\text{mean}}(V_{\text{eff}}) \cdot f(x, y) \cdot \rho_S$$

- $Q_{\text{mean}} = 2.3 \text{ pC}$
- $\rho_S = 60 \text{ M}\Omega/\text{sq}$  for anode
- $\rho_S = 7 \text{ M}\Omega/\text{sq}$  for cathode

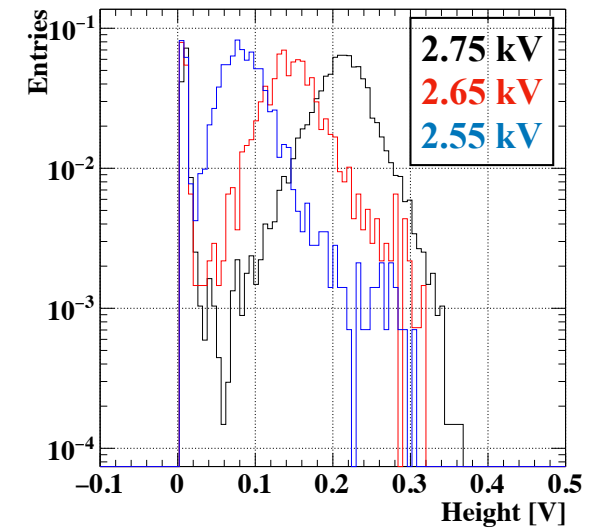


# Response to low-momentum $\mu^+$

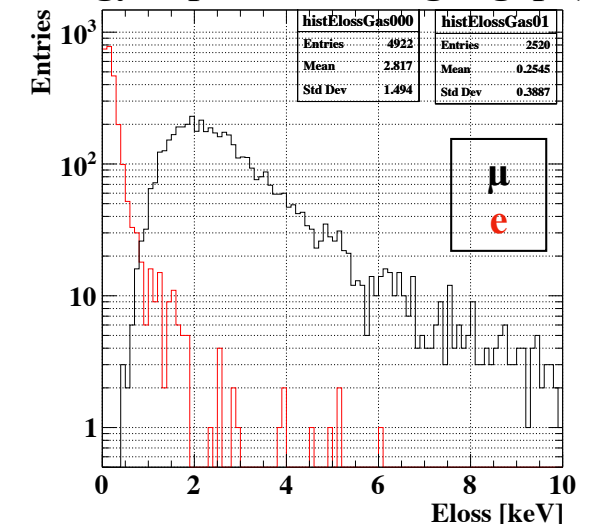


Decay  $e^+$  hit

Measured  $\mu$  height spectra

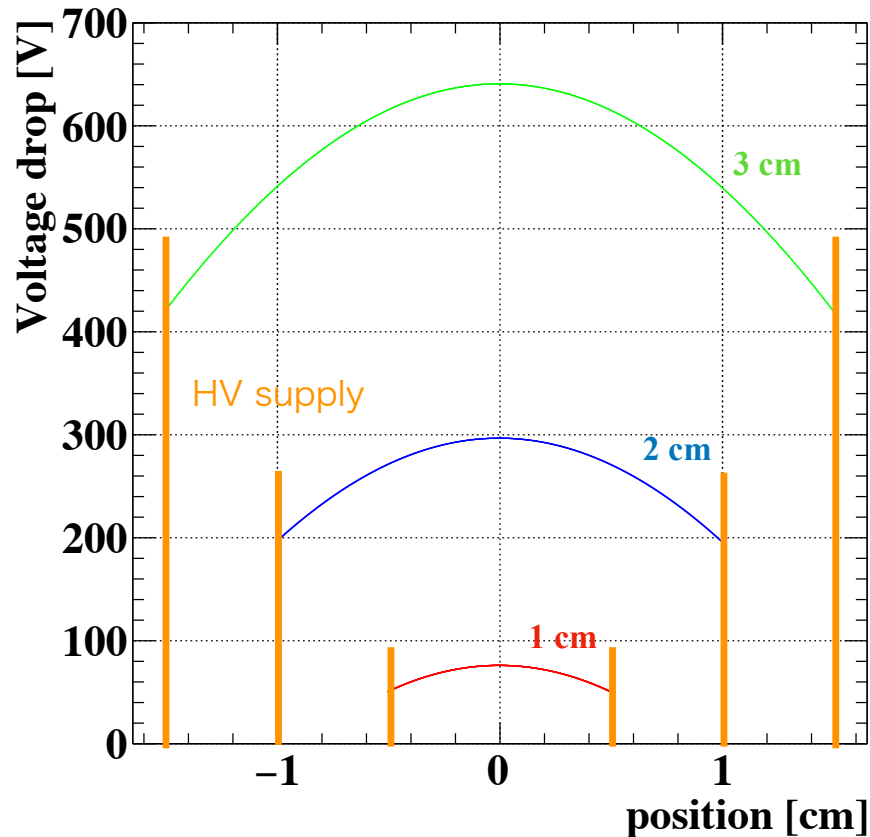


Energy deposit in RPC gas gap (MC)

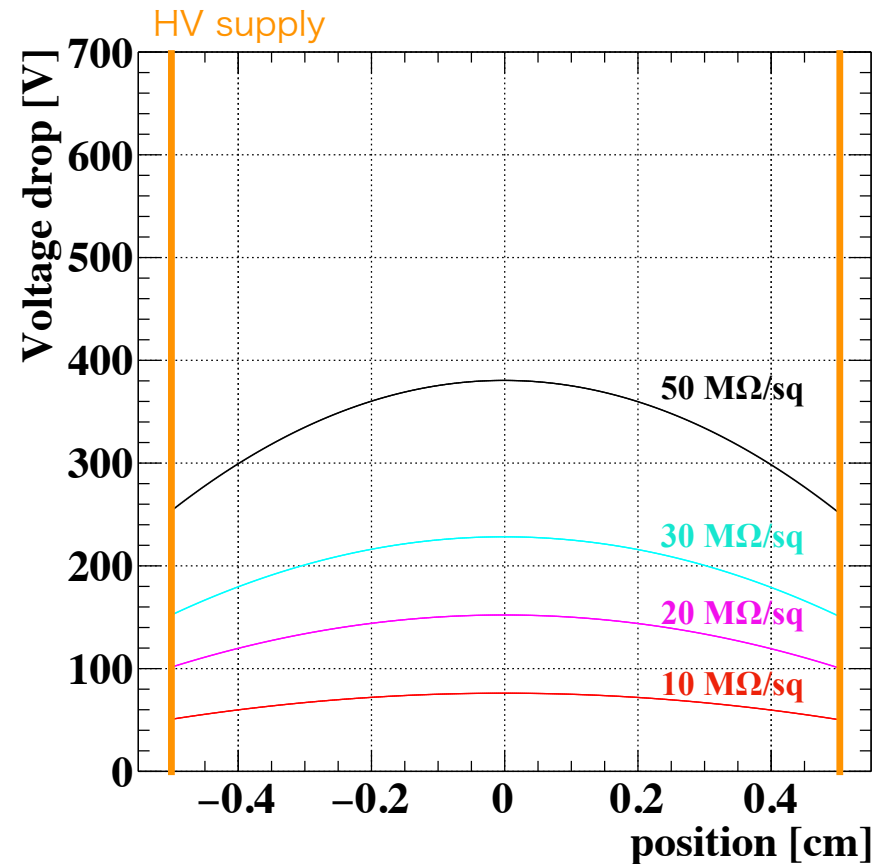


# Voltage drop estimation

$\ell_{\text{pitch}}$  dependence



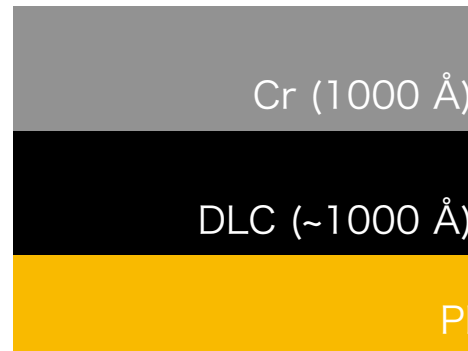
$\rho_S$  dependence



# Conductive pattern structure

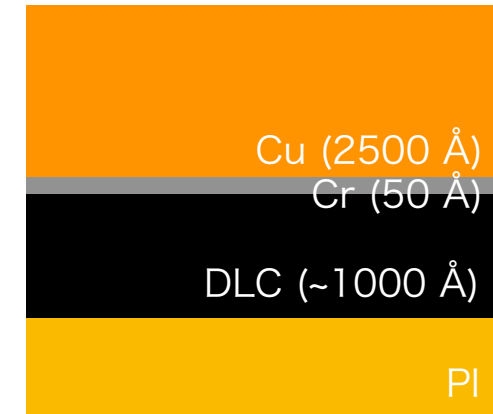
- Not trivial for metal to deposit on DLC
- **Cr can be well-connected**

1st trial

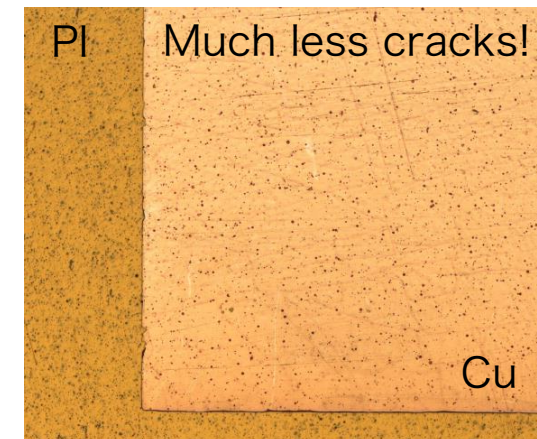
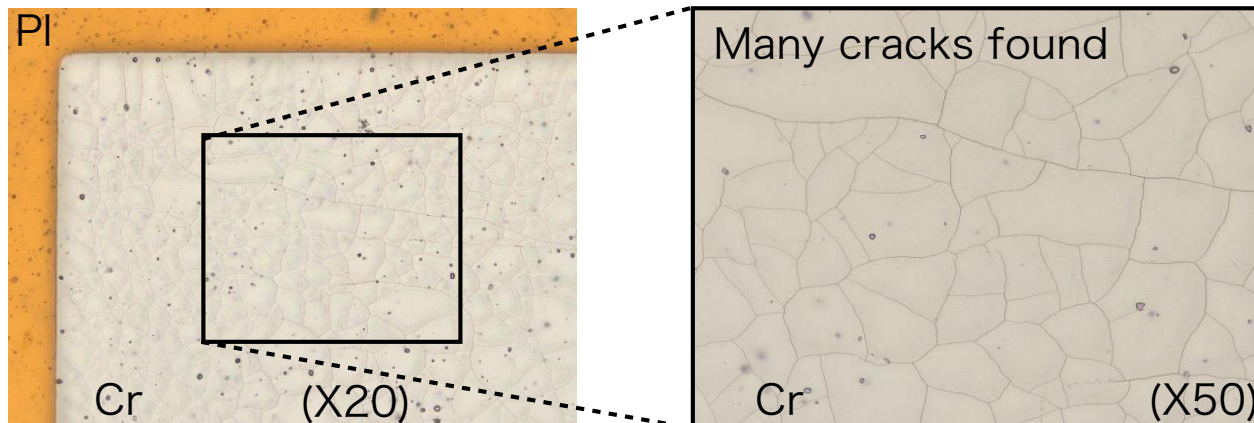


- Crack and removal of Cr
- Cr surface oxidised

Current structure

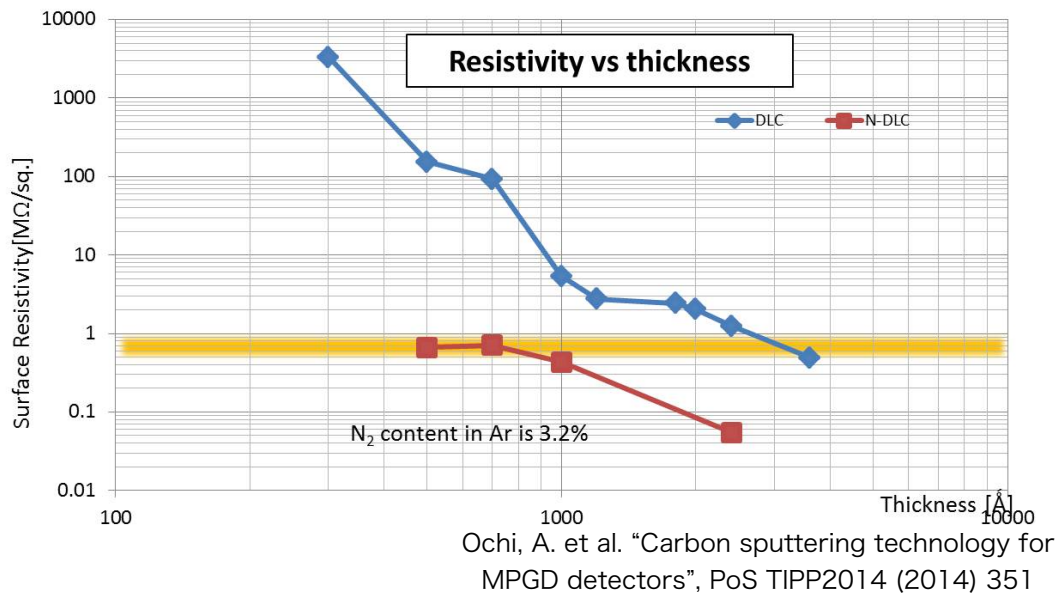


- **No problem found**



# Resistivity control

## DLC thickness



## Resistivity reduction by annealing

